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TFT | OLED | CHARACTER | GRAPHIC | UWVD | SEGMENT | CUSTOM

TFT Display Module

Part Number E24GA-I-CW1000-N

Overview:

- 2.4-inch TFT (42.72x60.06mm)
- MCU Interface
- 240x320 pixels
- All Viewing Angle
- White LED back-light
- Transmissive/ Normally Black
- No Touch Panel
- 1000 NITS
- Controller: ST7789V2
- RoHS Compliant

Description

This is a color active matrix TFT (Thin Film Transistor) LCD (Liquid Crystal Display) that uses amorphous silicon TFT as a switching device. This model is composed of a transmissive type TFT LCD Panel, driver circuit and a backlight unit. The resolution of the 2.4" TFT LCD contains 240(RGB)x320pixels and can display up to 262K colors.

TFT Features

Low Input Voltage: 3.3V

Display Colors: 262K

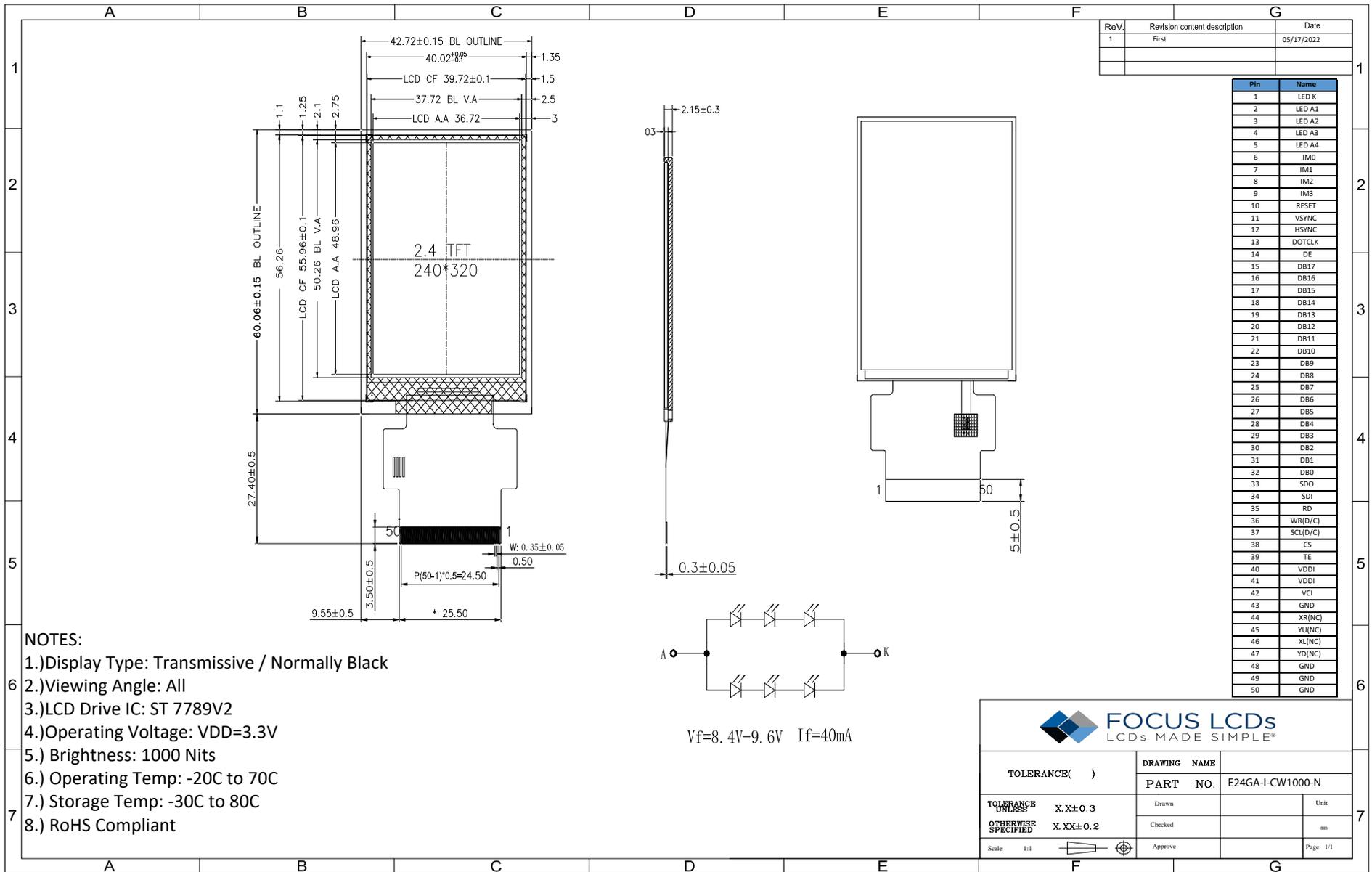
TFT Interface: MCU

General Information Items	Specification	Unit	Note
	Main Panel		
TFT Display area (AA)	36.72(H) x 48.96(V) (2.41 inch)	mm	-
Driver Element	TFT active matrix	-	-
Display Colors	262K	colors	-
Number of pixels	240(RGB) x 320	dots	-
TFT Pixel arrangement	RGB vertical stripe	-	-
Pixel Pitch	0.051(H)x0.153(V)	mm	-
Viewing angle	All	o'clock	-
Display mode	Transmissive, Normally Black	-	-
TFT Controller	ST7789V2	-	-
Operating temperature	-20-+70	°C	-
Storage temperature	-30-+80	°C	-

Mechanical Information

Item		Min	Typ.	Max	Unit	Note
Module Size	Horizontal (H)		42.72		mm	-
	Vertical (V)		60.06		mm	-
	Depth (D)		2.15		mm	-
	Weight		TBD		g	

1. Outline Dimensions



2. Input Terminal Pin Assignment

2.1 TFT

Pin	Name	I/O	Function																																																																																				
1	LEDK	P	Power for Backlight (Cathode)																																																																																				
2	LEDA1	P	Power for Backlight (anode)																																																																																				
3	LEDA2	P	Power for Backlight (anode)																																																																																				
4	LEDA3	P	Power for Backlight (anode)																																																																																				
5	LEDA4	P	Power for Backlight (anode)																																																																																				
6	IM0	I	<table border="1"> <thead> <tr> <th>IM3</th> <th>IM2</th> <th>IM1</th> <th>IM0</th> <th>80-16bit parallel I/F</th> <th>DB[7:0]</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>80-16bit parallel I/F</td> <td>DB[7:0]</td> </tr> <tr> <td>0</td> <td>0</td> <td>0</td> <td>1</td> <td>80-16bit parallel I/F</td> <td>DB[15:0]</td> </tr> <tr> <td>0</td> <td>0</td> <td>1</td> <td>0</td> <td>80-16bit parallel I/F</td> <td>DB[8:0]</td> </tr> <tr> <td>0</td> <td>0</td> <td>1</td> <td>1</td> <td>80-16bit parallel I/F</td> <td>DB[17:0]</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>1</td> <td>3-line 9bit serial I/F</td> <td>SDA: in/out</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> <td>0</td> <td>2 data lane serial I/F</td> <td>SDA: in/out WRX: in</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> <td>0</td> <td>4-line 8bit serial I/F</td> <td>SDA: in/out</td> </tr> <tr> <td>1</td> <td>0</td> <td>0</td> <td>0</td> <td>80-16bit parallel I/F II</td> <td>DB[17:10] DB[8:1]</td> </tr> <tr> <td>1</td> <td>0</td> <td>0</td> <td>1</td> <td>80-16bit parallel I/F II</td> <td>DB[17:10]</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> <td>0</td> <td>80-16bit parallel I/F II</td> <td>DB[17:0]</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> <td>1</td> <td>80-16bit parallel I/F II</td> <td>DB[17:9]</td> </tr> <tr> <td>1</td> <td>1</td> <td>0</td> <td>1</td> <td>3-line 9bit serial I/F II</td> <td>SDA: in/ SDO: out</td> </tr> <tr> <td>1</td> <td>1</td> <td>1</td> <td>0</td> <td>4-line 8bit serial I/F II</td> <td>SDA:in/ SDO: out</td> </tr> </tbody> </table>	IM3	IM2	IM1	IM0	80-16bit parallel I/F	DB[7:0]	0	0	0	0	80-16bit parallel I/F	DB[7:0]	0	0	0	1	80-16bit parallel I/F	DB[15:0]	0	0	1	0	80-16bit parallel I/F	DB[8:0]	0	0	1	1	80-16bit parallel I/F	DB[17:0]	0	1	0	1	3-line 9bit serial I/F	SDA: in/out	0	1	1	0	2 data lane serial I/F	SDA: in/out WRX: in	0	1	1	0	4-line 8bit serial I/F	SDA: in/out	1	0	0	0	80-16bit parallel I/F II	DB[17:10] DB[8:1]	1	0	0	1	80-16bit parallel I/F II	DB[17:10]	1	0	1	0	80-16bit parallel I/F II	DB[17:0]	1	0	1	1	80-16bit parallel I/F II	DB[17:9]	1	1	0	1	3-line 9bit serial I/F II	SDA: in/ SDO: out	1	1	1	0	4-line 8bit serial I/F II	SDA:in/ SDO: out
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7	IM1	I																																																																																					
8	IM2	I																																																																																					
9	IM3	I																																																																																					
10	RESET	I	Reset Input Signal																																																																																				
11	VSYNC	I	DPI: Frame Sync Signal - Fix to DGND level when not in use																																																																																				
12	HSYNC	I	DPI: Line Sync Signal - Fix to DGND level when not in use																																																																																				
13	DOTCLK	I	DPI: Dot Clock Signal - Fix to IOVCC level when not in use																																																																																				
14	DE	I	DPI: Data Enable Input Signal - Fix to DGND level when not in use																																																																																				
15	DB17	I/O	Corresponding RGB Data Component																																																																																				
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30	DB2	I/O																																																																																					
31	DB1	I/O																																																																																					
32	DB0	I/O																																																																																					
33	SDO	O	Serial Data Output																																																																																				
34	SDI	I/O	Serial Data Input/Output bi-directional pin																																																																																				
35	RD	I	DBI Type B: Serves at read signal																																																																																				
36	WR(D/C)	I	DBI Type B; WRX Pin, serves as write strobe signal ; DBI Type C: SCL Pin as serial clock when operating in serial interface. Fix to IOVCC or DGND level when not in use.																																																																																				
37	D/C(SCL)	I	DBI Type B: Data/Command Selection Pin; Low: Command; High: Parameter																																																																																				
38	CS	I	DBI Type B: Chip select input signal; Low - Chip selected and accessible; High - Chip is not selected and not accessible. Fix to IOVCC or DGND level when not in use.																																																																																				
39	TE	O	Tearing Effect Output Signal - Leave open when not in use.																																																																																				
40	VDDI	P	Supply voltage to digital circuit																																																																																				
41	VDDI	P	Supply voltage to digital circuit																																																																																				
42	VCI	P	Supply voltage to LCD circuit																																																																																				
43	GND	P	Power Ground																																																																																				
44	XR(X+)	-	TP Signal (Not Connected)																																																																																				
45	YU(Y+)	-	TP Signal (Not Connected)																																																																																				
46	XL(X-)	-	TP Signal (Not Connected)																																																																																				
47	YD(Y-)	-	TP Signal (Not Connected)																																																																																				
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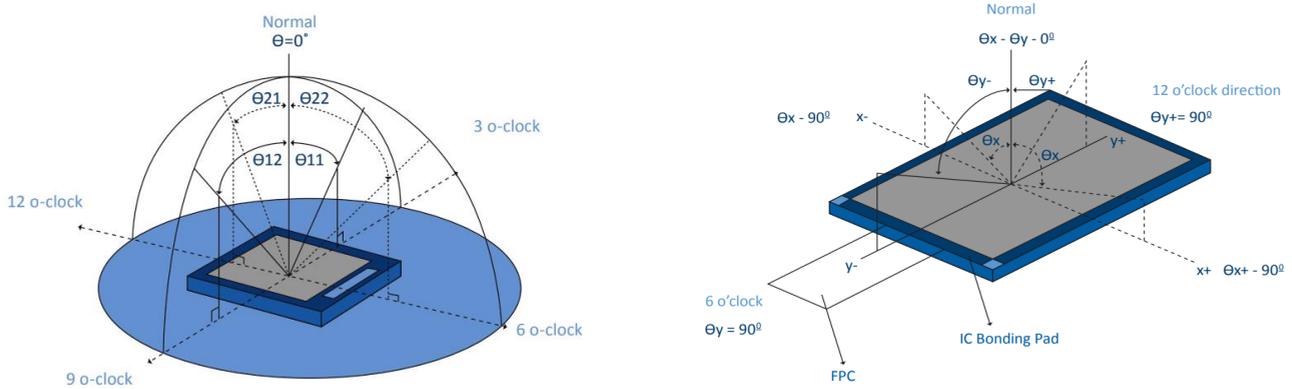
3. LCD Optical Characteristics

3.1 Optical Specifications

Item		Symbol	Condition	Min	Typ.	Max	Unit	Note
Response time		T_{r+T_f}	$\theta = 0$	35	45		ms	.
Contrast ratio		CR	At Optimized Viewing Angle	1000	1500	--	--	
Luminance Uniformity		ΔL		70	80		%	
Color Chromaticity (CIE 1931)	White	W_x	Normal viewing angle $\theta=0$	0.26	0.30	0.34	--	
		W_y		0.29	0.33	0.37		
Viewing Angle (ALL)	Hor.	θ_L	CR ≥ 10	70	80	--	Degree	
		θ_R		70	80	--		
	Ver.	θ_T		70	80	--		
		θ_B		70	80	--		

Optical Specification Reference Notes:

(1) Definition of Viewing Angle: The viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3,9 o'clock direction and the vertical or 6,12 o'clock direction with respect to the optical axis which is normal to the LCD surface.

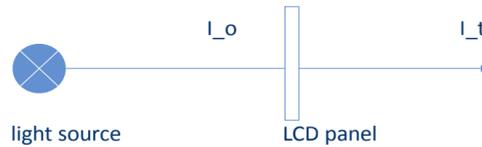


(2) Definition of Contrast Ratio (Cr): measured at the center point of panel. The contrast ratio (Cr) measured on a module, is the ratio between the luminance (Lw) in a full white area (R=G=B=1) and the luminance (Ld) in a dark area (R=G=B=0).

$$Cr = \frac{Lw}{Ld}$$

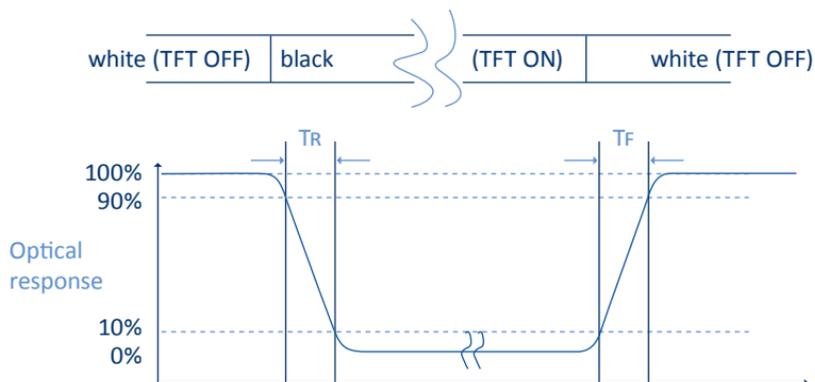
(3) Definition of transmittance (T%): The transmittance of the panel including the polarizers is measured with electrical driving. The equation for transmittance Tr is:

$$Tr = \frac{I_t}{I_o} \times 100\%$$



I_o = the brightness of the light source.
 I_t = the brightness after panel transmission

(4) Definition of Response Time (Tr, Tf): The rise time 'Tr' is defined as the time for luminance to change from 90% to 10% as a result of a change of the electrical condition. The fall time 'Tf' is defined as the time for luminance to change from 10% to 90% as a result of a change of the electrical condition.



(5) Definition of Color Gamut:

Measuring machine CFT-01. NTSC's Primaries: $R(x,y,Y), G(x,y,Y), B(x,y,Y)$. FPM520 of Westar Display Technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics. The color chromaticity shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.

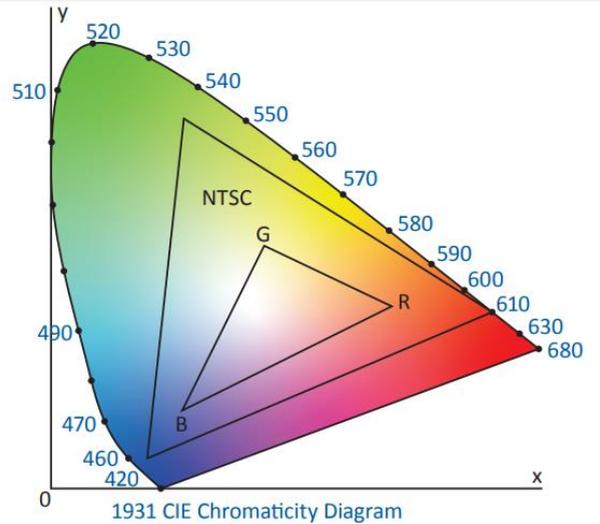
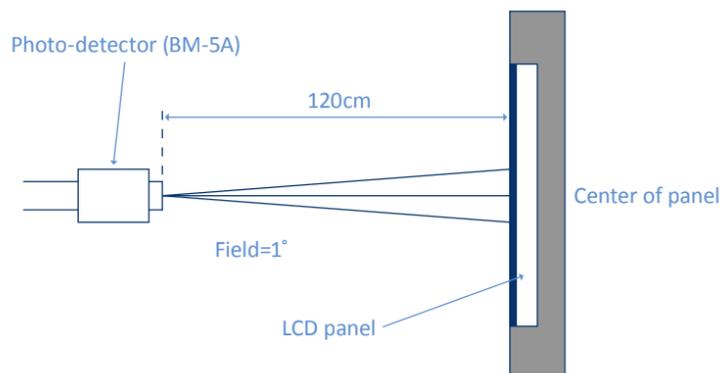
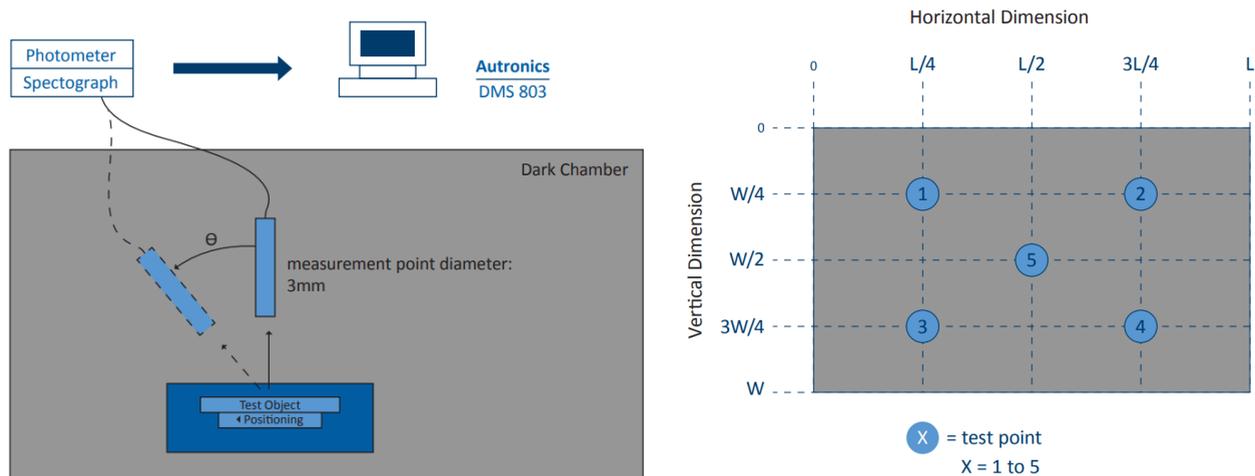


Fig. 1931 CIE chromacity diagram

$$\text{Color gamut: } S = \frac{\text{Area of RGB triangle}}{\text{Area of NTSC triangle}} \times 100\%$$

(6) Definition of Optical Measurement Setup:

The LCD module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 20 minutes.



4. TFT Electrical Characteristics

4.1 Absolute Maximum Rating (Ta=25 °C, VSS=0V)

Characteristics	Symbol	Min	Max	Unit
Digital Supply Voltage	VDD	VSS-0.3	5.0	V

NOTE: If the absolute maximum rating of the above parameters is exceeded, even momentarily, the quality of the product may be degraded. Absolute maximum ratings specify the values which the product may be physically damaged if exceeded. Be sure to use the product within the range of the absolute maximum ratings.

4.2 DC Electrical Characteristics

Characteristics	Symbol	Min	Typ.	Max	Unit	Note
LED Voltage	V _F	-	9.6	-	V	
LED Current	I _F	-	40	-	mA	
Power Consumption	P _{BL}	-	384	-	mW	
DRIVE MODE	Constant current					

Note (1) Where I_F = 40MA, V_F = 9.6V P_{BL} = V_F × I_F

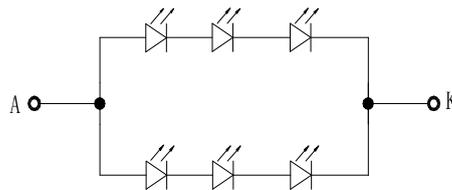
4.3 LED Backlight Characteristics

The backlight system is edge lighting type with 6 white LED CHIPS.

Item	Symbol	Min	Typ.	Max	Unit	Note
Forward Current	I _F	--	--	40	mA	
Forward Voltage	V _F	--	9.6	--	V	
LCM Luminance	LV	--	1000	--	cd/m ²	Note 3
LED lifetime	Hr	50000	--	--	hour	Note1 & 2
Uniformity	AV _g	80	--	--	%	Note 3

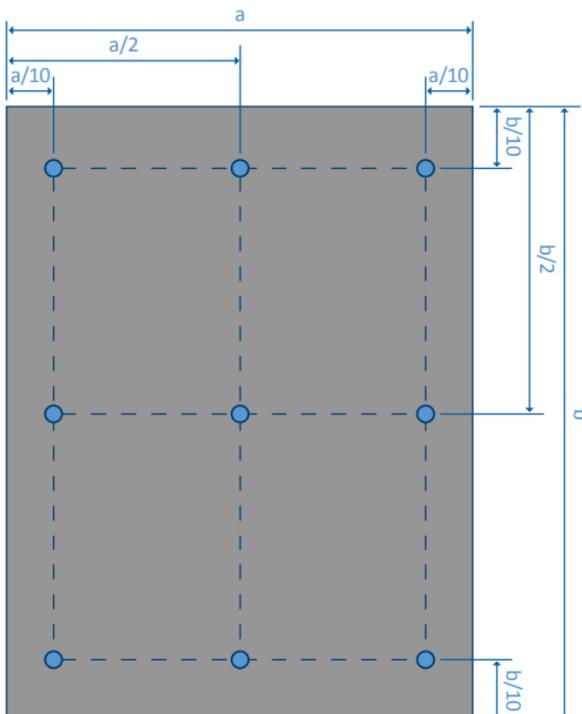
Note 1: LED lifetime (Hr) can be defined as the time in which it continues to operate under the condition: T_a=25 ±3 °C, typical IL value indicated in the above table until the brightness becomes less than 50%.

Note 2: The “LED lifetime” is defined as the module brightness decrease to 50% original brightness at T_a=25°C and I_L = 40mA. The LED lifetime could be decreased if operating I_L is larger than 40mA. The constant current driving method is suggested.



V_f=8.4V-9.6V I_f=40mA

Note 3: Luminance Uniformity of these 9 points is defined as below:



$$\text{Luminance} = \frac{\text{Total Luminance of 9 points}}{9}$$

$$\text{Uniformity} = \frac{\text{minimum luminance in 9 points(1-9)}}{\text{maximum luminance in 9 points(1-9)}}$$

5.0 TFT AC Characteristics

5.1 Interface Timing

*For the interface timing diagram, see diagram on page 49 of the data sheet for controller IC ST77789V2.
The data sheet can be found here <https://focuslcds.com/content/ST7789V2.pdf>*

6.0 Quality Inspection Standards

For TFT quality inspection standards, please see the following link: <https://focuslcds.com/tft-quality-inspection-standards/>

7.0 Cautions and Handling Precautions

7.1 Handling and Operating the Module

1. When the module is assembled, it should be attached to the system firmly. Do not warp or twist the module during assembly work.
2. Protect the module from physical shock or any force. In addition to damage, this may cause improper operation or damage to the module and back-light unit.
3. Note that polarizer is very fragile and could be easily damaged. Do not press or scratch the surface.
4. Do not allow drops of water or chemicals to remain on the display surface. If you have the droplets for a long time, staining and discoloration may occur.
5. If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
6. The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane. Do not use ketene type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might cause permanent damage to the polarizer due to chemical reaction.
7. If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs, or clothes, it must be washed away thoroughly with soap.
8. Protect the module from static; it may cause damage to the CMOS ICs.
9. Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
10. Do not disassemble the module.
11. Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
12. Pins of I/F connector shall not be touched directly with bare hands.
13. Do not connect, disconnect the module in the "Power ON" condition.
14. Power supply should always be turned on/off by the item Power On Sequence & Power Off Sequence.

7.2 Storage and Transportation

1. Do not leave the panel in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 0 to 35 °C and relative humidity of less than 70%.
2. Do not store the TFT-LCD module in direct sunlight.
3. The module shall be stored in a dark place. When storing the modules for a long time, be sure to adopt effective measures for protecting the modules from strong ultraviolet radiation, sunlight, or fluorescent light.
4. It is recommended that the modules should be stored under a condition where no condensation is allowed. Formation of dewdrops may cause an abnormal operation or a failure of the module. In particular, the greatest possible care should be taken to prevent any module from being operated where condensation has occurred inside.
5. This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed.