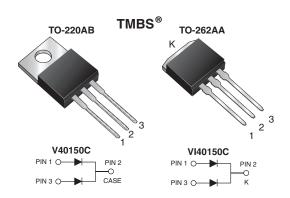


Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.55 \text{ V}$ at $I_F = 5 \text{ A}$



D²PAK (TO-263AB)



DESIGN SUPPORT TOOLS

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PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 20 A				
V _{RRM}	150 V				
I _{FSM}	160 A				
V _F at I _F = 20 A	0.75 V				
T _J max.	150 °C				
Package	TO-220AB, TO-262AA, D ² PAK (TO-263AB)				
Circuit configuration	Common cathode				

FEATURES

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation

 Solder bath temperature 275 °C max. 10 s, per JESD 22-B106



- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C (for TO-263AB package)
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

MECHANICAL DATA

Case: TO-220AB, TO-262AA, and D²PAK (TO-263AB), Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)						
PARAMETER		SYMBOL	V40150C	VB40150C	VI40150C	UNIT
Maximum repetitive peak reverse voltage		V_{RRM}	150			V
Maximum average forward rectified current (fig. 1)	per device	_	40			Α
	per diode	I _{F(AV)}	20			
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	160		А	
Voltage rate of change (rated V _R)		dV/dt	10 000		V/µs	
Operating junction and storage temperature ra	nge	T _J , T _{STG}		-55 to +150		°C

V40150C, VB40150C, VI40150C

Vishay General Semiconductor

ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	V _F ⁽¹⁾	0.69	-	V	
	I _F = 10 A			0.84	-		
	I _F = 20 A			1.15	1.43		
	I _F = 5 A	T _A = 125 °C		0.55	-		
	I _F = 10 A			0.64	-		
	I _F = 20 A			0.75	0.82		
Reverse current per diode	V _R = 100 V	T _A = 25 °C	I _R (2)	2	-	μΑ	
		T _A = 125 °C		2.5	-	mA	
	V _R = 150 V	T _A = 25 °C		-	250	μΑ	
		T _A = 125 °C		5	25	mA	

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	V40150C	VB40150C	VI40150C	UNIT	
Typical thermal resistance per diode	$R_{ heta JC}$	1.8			°C/W	

ORDERING INFORMATION (Example)							
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
TO-220AB	V40150C-M3/4W	1.89	4W	50/tube	Tube		
TO-262AA	VI40150C-M3/4W	1.46	4W	50/tube	Tube		
TO-263AB	VB40150C-M3/I	1.39	I	800/reel	Tape and reel		

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

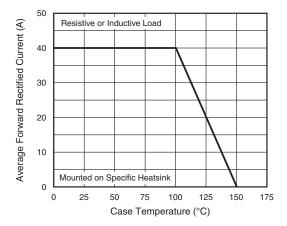
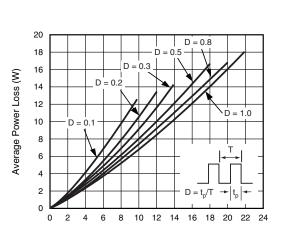


Fig. 1 - Maximum Forward Current Derating Curve



Average Forward Current (A)

Fig. 2 - Forward Power Dissipation Characteristics

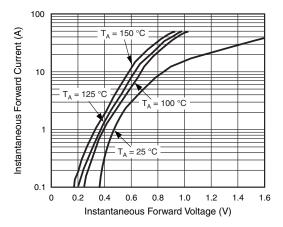


Fig. 3 - Typical Instantaneous Forward Characteristics

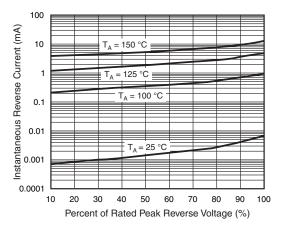


Fig. 4 - Typical Reverse Characteristics

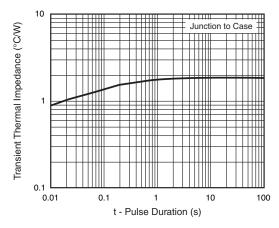


Fig. 5 - Typical Transient Thermal Impedance

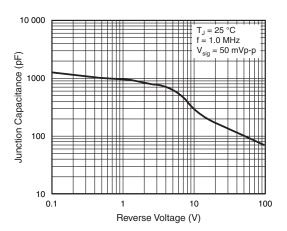


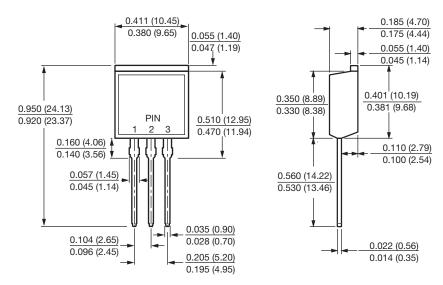
Fig. 6 - Typical Junction Capacitance



PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

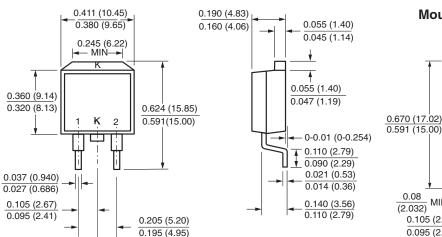
TO-220AB 0.415 (10.54) 0.380 (9.65) 0.185 (4.70) 0.161 (4.08) 0.175 (4.44) 0.139 (3.53) 0.055 (1.39) 0.113 (2.87) 0.045 (1.14) 0.103 (2.62) 0.603 (15.32) 0.635 (16.13) 0.573 (14.55) 0.625 (15.87) PIN 0.350 (8.89) 2 0.330 (8.38) 0.160 (4.06) 1.148 (29.16) 0.140 (3.56) 1.118 (28.40) 0.110 (2.79) 0.100 (2.54) 0.057 (1.45) 0.045 (1.14) 0.560 (14.22) 0.530 (13.46) 0.035 (0.90) 0.028 (0.70) 0.104 (2.65) 0.022 (0.56) 0.096 (2.45) 0.205 (5.20) 0.014 (0.36) 0.195 (4.95)

TO-262AA

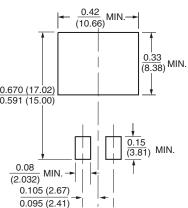




D²PAK (TO-263AB)



Mounting Pad Layout





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