

SGLS170 - JUNE 2003

NANOPOWER PUSH-PULL OUTPUT COMPARATOR

FEATURES

- Controlled Baseline
 - One Assembly/Test Site, One Fabrication Site
- Extended Temperature Performance of -40°C to 125°C
- Enhanced Diminishing Manufacturing Sources (DMS) Support
- Enhanced Product-Change Notification
- Qualification Pedigree[†]
- Low Supply Current . . . 560 nA
- Input Common-Mode Range Exceeds the Rails . . . -0.1 V to V_{CC} + 5 V
- Supply Voltage Range . . . 2.7 V to 16 V
- Reverse Battery Protection Up to 18 V
- Push-Pull CMOS Output Stage
- Ultrasmall Packaging
 - 5-Pin SOT-23
- Universal Op-Amp EVM (Reference SLOU060 for more information)

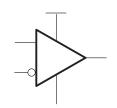
† Component qualification in accordance with JEDEC and industry standards to ensure reliable operation over an extended temperature range. This includes, but is not limited to, Highly Accelerated Stress Test (HAST) or biased 85/85, temperature cycle, autoclave or unbiased HAST, electromigration, bond intermetallic life, and mold compound life. Such qualification testing should not be viewed as justifying use of this component beyond specified performance and environmental limits.

APPLICATIONS

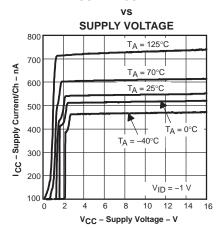
- Portable Battery Monitoring
- Security Detection Systems

DESCRIPTION

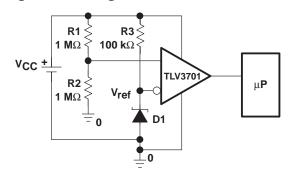
The TLV3701 is part of Texas Instruments' first family of nanopower comparator with only 560 nA supply current, which make this device ideal for low power applications.



SUPPLY CURRENT



high side voltage sense circuit





Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



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DESCRIPTION (continued)

The TLV3701 has a minimum operating supply voltage of 2.7 V over the extended temperature range $T_A = -40^{\circ}$ C to 125°C, while having an input common-mode range of -0.1 to $V_{CC} + 5$ V. The low supply current makes it an ideal choice for battery powered portable applications where quiescent current is the primary concern. Reverse battery protection guards the amplifier from an over-current condition due to improper battery installation. For harsh environments, the inputs can be taken 5 V above the positive supply rail without damage to the device.

This device is available in the small SOT-23 package. Other package options may be made available upon request.

A SELECTION OF OUTPUT COMPARATORST

DEVICE	V _{CC}	V _{IO} (μV)	I _{CC} /Ch (μA)	I _{IB} (pA)	tPLH (μ s)	tPHL (μs)	t f (μ s)	t _r (μ s)	RAIL-TO- RAIL	OUTPUT STAGE
TLV370x	2.5 – 16	250	0.56	80	56	83	22	8	I	PP
TLV340x	2.5 – 16	250	0.47	80	55	30	5	-	I	OD
TLC3702/4	3 – 16	1200	9	5	1.1	0.65	0.5	0.125	_	PP
TLC393/339	3 – 16	1400	11	5	1.1	0.55	0.22	-	_	OD
TLC372/4	3 – 16	1000	75	5	0.65	0.65	_	_	_	OD

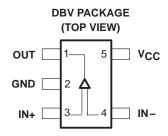
[†] All specifications are typical values measured at 5 V.

AVAILABLE OPTIONS†

	V	PACKAGED DEVICES			
TA	V _{IO} max AT 25°C	SOT-23 (DBV) [‡]	SYMBOL		
-40°C to 125°C	5000 μV	TLV3701QDBVREP	VBCE		

[†] Contact the local TI sales office for availability of other package options.

[‡]This package is only available taped and reeled with standard quantities of 3000 pieces per reel.





absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V _{CC} (see Note 1)	17 V
Differential input voltage, V _{ID}	
Input voltage range, V _I (see Notes 1 and 2)	\dots 0 to V_{CC} + 5 V
Input current range, I ₁	±10 mA
Output current range, I _O	±10 mA
Continuous total power dissipation	. See Dissipation Rating Table
Operating free-air temperature range, T _A	–40°C to 125°C
Maximum junction temperature, T _J	150°C
Storage temperature range, T _{stq}	–65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. All voltage values, except differential voltages, are with respect to GND.

DISSIPATION RATING TABLE

PACKAGE	^θ JC (°C/W)	^θ JA (°C/W)	$T_{\mbox{A}} \le 25^{\circ}\mbox{C}$ POWER RATING	T _A = 125°C POWER RATING
DBV	55	324.1	385 mW	77.1 mW

recommended operating conditions

		MIN	MAX	UNIT
	Single supply	2.7	16	.,
Supply voltage, V _{CC}	Split supply	±1.35	±8	V
Common-mode input voltage range, V _I	-0.1	V _{CC} +5	V	
Operating free-air temperature, TA	-40	125	°C	

electrical characteristics at specified operating free-air temperature, V_{CC} = 2.7 V, 5 V, 15 V (unless otherwise noted)

dc performance

	PARAMETER	TEST C	T _A †	MIN	TYP	MAX	UNIT	
V	lamint official voltages			25°C		250	5000	/
V _{IO}	Input offset voltage	$V_{IC} = V_{CC}/2,$	$R_S = 50 \Omega$	Full range			7000	μV
ανιο	Offset voltage drift			25°C		3		μV/°C
		V - 0 to 0 7 V	D- 50 O	25°C	55	72		
		$V_{IC} = 0 \text{ to } 2.7 \text{ V},$	$R_S = 50 \Omega$	Full range	50			
OMBB	Occurred to the second section	V 01-5V	D 50.0	25°C	60	76		.ID
CMRR	Common-mode rejection ratio	$V_{IC} = 0 \text{ to } 5 \text{ V},$	$R_S = 50 \Omega$	Full range	55			dB
		.,	D 500	25°C	65	88		
		$V_{IC} = 0 \text{ to } 15 \text{ V},$	$R_S = 50 \Omega$	Full range	60			
AVD	Large-signal differential voltage amplification			25°C		1000		V/mV

[†] Full range is -40°C to 125°C for Q suffix.



^{2.} Input voltage range is limited to 20 V max or V_{CC} + 5 V, whichever is smaller.

electrical characteristics at specified operating free-air temperature, V_{CC} = 2.7 V, 5 V, 15 V (unless otherwise noted) (continued)

input/output characteristics

	PARAMETER	TES	ST CONDITIONS	T _A †	MIN	TYP	MAX	UNIT
Ι.	hand effect assessed			25°C		20	100	4
lio	Input offset current	., ., ,,	D 50.0	Full range			1000	pA
	Land Management	$V_{IC} = V_{CC}/2$,	$KS = 20 \Omega$	25°C		80	250	4
IВ	I _{IB} Input bias current			Full range			2000	рA
ri(d)	Differential input resistance			25°C		300		ΜΩ
		V _{IC} = V _{CC} /2,	$I_{OH} = 2 \mu A$, $V_{ID} = 1 V$	25°C		V _C C- 0.08		
Vон	High-level output voltage			25°C	V _{CC} - 320			mV
		$V_{IC} = V_{CC}/2,$	$I_{OH} = -50 \mu\text{A}, V_{ID} = 1 \text{V}$	Full range	V _{CC} - 450			
		$V_{IC} = V_{CC}/2$,	$I_{OH} = 2 \mu A$, $V_{ID} = -1 V$	25°C		8		
VOL	Low-level output voltage	V: V/2	Jour - 50 u.A. V/m - 1 V/	25°C		80	200	mV
		VIC = VCC/2	$I_{OH} = 50 \mu\text{A}, V_{ID} = -1 \text{V}$	Full range			300	

[†] Full range is -40°C to 125°C for Q suffix.

power supply

	PARAMETER	TEST CON	T _A †	MIN	TYP	MAX	UNIT	
I _{CC} Supply current		0		25°C		560	800	nA
		Output state high	Full range			1200		
			V 07V405V	25°C	75	100		
PSRR	Dower cumply rejection ratio	V _{IC} = V _{CC} /2 V, No load	$V_{CC} = 2.7 \text{ V to 5 V}$	Full range	70			dB
PSKK	Power supply rejection ratio		V 5 V to 45 V	25°C	85	105		uБ
			$V_{CC} = 5 \text{ V to } 15 \text{ V}$	Full range	80		·	

[†]Full range is -40°C to 125°C for Q suffix.

switching characteristics at recommended operating conditions (unless otherwise noted)

	PARAMETER	TEST CON	MIN	TYP	MAX	UNIT	
			Overdrive = 2 mV		240		
^t (PLH)	Propagation response time, low-to-high-level output (see Note 3)	f = 1 kHz,	Overdrive = 10 mV		64		μs
	output (occ recto o)	VSTEP = 100 mV,	Overdrive = 50 mV		36		
		C _L = 10 pF,	Overdrive = 2 mV		167		
t(PHL)	Propagation response time, high-to-low-level output (see Note 3)	V _{CC} = 2.7 V	Overdrive = 10 mV		67		
` ′	output (doe Hote o)		Overdrive = 50 mV		37		
t _r	Rise time	$C_L = 10 \text{ pF}, V_{CC} = 2.7 \text{ V}$			7		μs
tf	Fall time	$C_L = 10 \text{ pF}, V_{CC} = 2$	2.7 V		9		μs

NOTE 3: The response time specified is the interval between the input step function and the instant when the output crosses 1.4 V. Propagation responses are longer at higher supply voltages, refer to Figures 11–16 for further details.



TYPICAL CHARACTERISTICS

Table of Graphs

			FIGURE
	Input bias/offset current	vs Free-air temperature	1
VOL	Low-level output voltage	vs Low-level output current	2, 4, 6
Vон	High-level output voltage	vs High-level output current	3, 5, 7
		vs Supply voltage	8
ICC	Supply current	vs Free-air temperature	9
	Output fall time/rise time	vs Supply voltage	10
	Low-to-high level output response for various input overdrives		11, 13, 15
	High-to-low level output response for various input overdrives		12, 14, 16

VS FREE-AIR TEMPERATURE VCC = 15 V VCC = 15 V VCC = 15 V VCC = 15 V IIB VCC = 15 V IIB VCC = 15 V VCC = 1

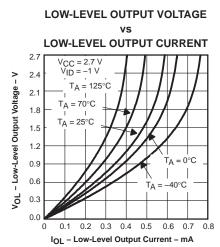
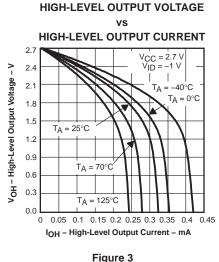
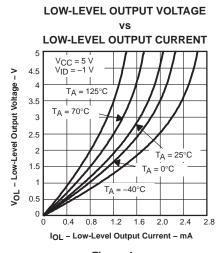
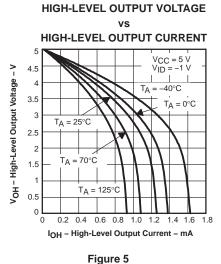


Figure 1

Figure 2



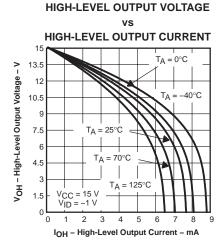




re 3 Figure 4

TYPICAL CHARACTERISTICS

LOW-LEVEL OUTPUT VOLTAGE LOW-LEVEL OUTPUT CURRENT V_{CC} = 15 V V_{ID} = -1 V 13.5 V_{OL} - Low-Level Output Voltage - V 12 $T_A = 125^{\circ}C$ 10.5 T_A = 70°C T_A = 25°C 7.5 $T_A = 0^{\circ}C$ 2 4 5 6 7 8 IOL - Low-Level Output Current - mA



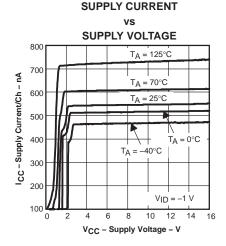


Figure 8

Figure 6

.9....

SUPPLY CURRENT vs

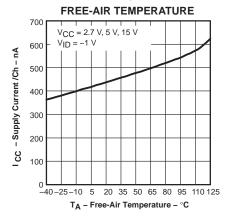


Figure 7

OUTPUT RISE/FALL TIME

vs

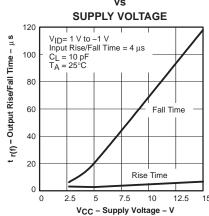


Figure 9

LOW-TO-HIGH OUTPUT RESPONSE FOR VARIOUS INPUT OVERDRIVES

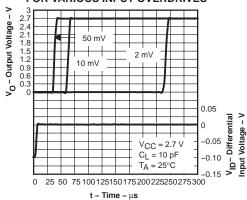


Figure 10
HIGH-TO-LOW LEVEL OUTPUT RESPONSE

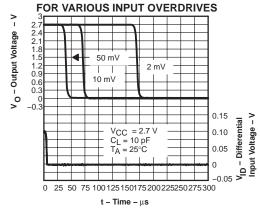


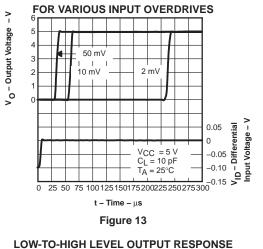
Figure 11

Figure 12



TYPICAL CHARACTERISTICS

LOW-TO-HIGH LEVEL OUTPUT RESPONSE



LOW-TO-HIGH LEVEL OUTPUT RESPONSE

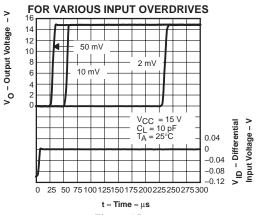
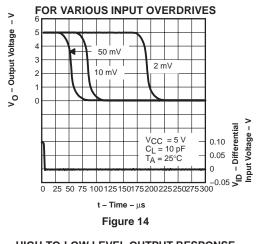
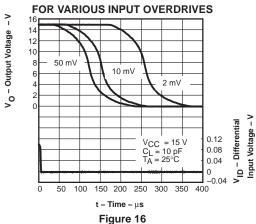


Figure 15

HIGH-TO-LOW LEVEL OUTPUT RESPONSE



HIGH-TO-LOW LEVEL OUTPUT RESPONSE



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PACKAGE OPTION ADDENDUM

10-Dec-2020

PACKAGING INFORMATION

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Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead finish/ Ball material	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
TLV3701QDBVREP	ACTIVE	SOT-23	DBV	5	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	VBCQ	Samples
V62/04726-01XE	ACTIVE	SOT-23	DBV	5	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	VBCQ	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (CI) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead finish/Ball material Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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PACKAGE OPTION ADDENDUM

10-Dec-2020

OTHER QUALIFIED VERSIONS OF TLV3701-EP:

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• Automotive: TLV3701-Q1

NOTE: Qualified Version Definitions:

• Catalog - TI's standard catalog product

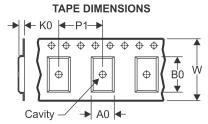
• Automotive - Q100 devices qualified for high-reliability automotive applications targeting zero defects

PACKAGE MATERIALS INFORMATION

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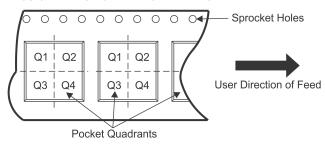
TAPE AND REEL INFORMATION





A0	
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device	Package Type	Package Drawing			Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TLV3701QDBVREP	SOT-23	DBV	5	3000	180.0	9.0	3.15	3.2	1.4	4.0	8.0	Q3

www.ti.com 25-Sep-2019



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TLV3701QDBVREP	SOT-23	DBV	5	3000	182.0	182.0	20.0

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