PCN Number: 201		201611	161122000		PCN Date:		Nov 30, 2016	
Title:         TPS3813K33MDBVREP Design Change								
Customer Contact:		PC	N Manager	-	Dept:		Quality Services	
Proposed 1 <sup>st</sup> Ship Date:		Ma	ay 30, 2017	Estimated San Availability:	stimated Sample vailability:		Date provided at sample request.	
Change Ty	ype:							
Assembly Site			Assembly Process				y Materials	
Design			Electrical Specification		Ц	Mechanical Specification		
Test S			Packing/Shipping/Labeling				Test Process	
	Bump Site		Wafer Bump			Wafer Bump Process Wafer Fab Process		
	Fab Site		Wafer Fab Materials					
			Part number change PCN Details					
Descriptio	on of Change:		PCN	Details				
		m of a c	lie revision cha	ande to select de	vice	s A design	change was	
			a die revision change to select devices. A design change was ce performance by addressing limitations in the test mode (TM)					
			etal2 were made to disable the test mode. The logic inputs for TM					
			ed to ground and the TM decision circuitry was disconnected. This					
design cha	nge does not a	ffect the	form, fit or fu	nction nor the ele	ectri	cal specific	cations of the device.	
-								
Reason fo	r Change:							
Improved p	product perforn	nance						
Anticipate	ed impact on I	<sup>-</sup> orm, Fi	t, Function, O	Quality or Relia	bilit	y (positiv	ve / negative):	
None								
	-			from this PCN:				
Die Rev de	signator will ch	ange as	shown in the	table and sample	lab	el below:		
Current	No							
A								
Comple product chipping label to indicate die row leasting (not actual product label)								
Sample product shipping label to indicate die rev location (not actual product label)								
INSTRUMENTS								
INSTRUMENTS G4								
MSL 2 /260C/1 YEAR SEAL DT (31T)LOT: 3959047MLA MSL 1 /235C/UNLIM 03/29/04								
OPT: ITEM: 39								
DI , 54 /1 TO 1750 (20L) CSO: SHE (21L) CCO:USA								
LDL: JA (L)IV.IIJU (22L) ASO: MLA (23L) ACO: MYS								
Droduct A	flactod							
Product A								
2T13K33M	DBVREPG4	TPS381	3K33MDBVREP	V62/06627-0	1XE			

## **Qualification Report**

## TPS3813K33MDBVREP Die-Rev B in Lingsen 6-pin SOT-23

Approve Date 04-Nov-2016

## Product Attributes

Attributes	Qual Device: TPS3813K33MDBVREP	QBS Process Reference: TLV3701IDBV	
Assembly Site	LINGSEN	LINGSEN	
Package Family	SOT23	SOT23	
Wafer Fab Supplier	DFAB	DFAB	
Wafer Process	LBC3S	LBC3S	

- QBS: Qual By Similarity

- Qual Device TPS3813K33MDBVREP is qualified at LEVEL1-260CG

## Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	Test Name / Condition	Duration	Qual Device: TPS3813K33MDBVREP	QBS Process Reference: TLV3701IDBV
ED	Electrical Characterization	Per Datasheet Parameters	1/30/0	-
CDM	ESD - CDM	1000 V	-	3/9/0
HBM	ESD - HBM	900 V	-	3/12/0
LU	Latch-up	(per JESD78)	-	3/18/0
HAST	Biased HAST, 130C/85%RH	96 Hours	-	3/231/0
HTOL	Life Test, 150C	300 Hours	-	3/231/0
HTSL	High Temp. StorageBake, 170C	420 Hours	-	1/77/0
TC	Temperature Cycle, -65/150C	500 Cycles	-	3/231/0
UHAST	Unbiased HAST, 130C/85%RH	96 Hours	-	3/231/0
SD	Solderability	Pb Free	-	3/91/0

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
 The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours
 The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles
 Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

Green/Pb-free Status: Qualified Pb-Free (SMT) and Green

For questions regarding this notice, e-mails can be sent to the regional contacts shown below, or you can contact your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com