



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BVsss	Rss(on) Typ	Is Max T _A = +25°C
20V	$23.4m\Omega$ @ V _{GS} = $3.8V$	6.3A

Description

This new generation MOSFET is designed to minimize the on-state resistance (Rss(on)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Battery managements
- Load switches
- **Battery protections**

Halogen and Antimony Free. "Green" Device (Note 3) For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please

Features

contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

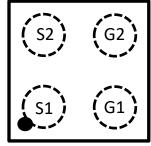
Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)

Mechanical Data

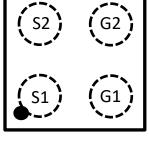
- Package: X4-DSN1111-4
- Terminal Connections: See Diagram Below
- Moisture Sensitivity: Level 1 per J-STD-020

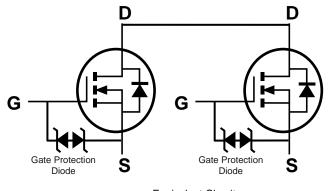
CSP with Footprint 1.15mm x 1.15mm Height = 0.120mm (Typical) for Low Profile

- Terminals: Finish NiAu. Solderable per MIL-STD-202, Method
- Weight: 0.0005 grams (Approximate)



Top View





Equivalent Circuit

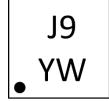
Ordering Information (Note 4)

Part Number	Dackers	Packing		
Part Number	Package	Qty.	Carrier	
DMN2030UCA4-7	X4-DSN1111-4	3000	Tape & Reel	

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



J9 = Product Type Marking Code YW = Date Code Marking Y or \overline{Y} = Year (ex: 2 = 2022) W or \overline{W} = Week (ex: a = Week 27; z Represents Week 52 and 53)

Date Code Key

Year	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	2	3	4	5	6	7	8	9	0	1	2	3
Week 1-26			27-52				53					
Code	Code A-Z				а	-Z				Z		



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit			
Source-Source Voltage	Vsss	20	V			
Gate-Source Voltage	V_{GSS}	±12	V			
0 (0 0 1/11 5) // 45/	Steady	T _A = +25°C		6.3		
Continuous Source Current (Note 5) V _{GS} = 4.5V	State	T _A = +70°C	Is	5.0	Α	
	Steady	T _A = +25°C		5.0	•	
Continuous Source Current (Note 5) V _{GS} = 2.5V	State	$T_A = +70$ °C	Is	4.0	A	
Pulsed Source Current (Note 6)	lsм	38	А			

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 7)	P _D	1.0	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 7)	Reja	126	°C/W
Power Dissipation (Note 5)	PD	1.9	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	Reja	65	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 8)								
Source-Source Breakdown Voltage	BVsss	20		_	V	$V_{GS} = 0V$, $I_{S} = 1mA$		
Zero Gate Voltage Drain Current T _J = +25°C	Isss	_	_	1	μA	Vss = 16V, Vgs = 0V		
Cata Cauraa Laakaga		_	_	±1	μA	$V_{GS} = \pm 8V$, $V_{SS} = 0V$		
Gate-Source Leakage	Igss	_	_	±0.1	μA	$V_{GS} = \pm 3.8V, V_{SS} = 0V$		
ON CHARACTERISTICS (Note 8)								
Gate Threshold Voltage	Vgs(TH)	0.35	1	1.4	V	$V_{SS} = 10V$, $I_{S} = 0.16mA$		
		20	21.4	32		$V_{GS} = 4.5V, I_{S} = 1.7A$		
		20.5	22.4	34		V _G S = 4.1V, I _S = 1.7A		
		21	22.7	35		$V_{GS} = 4.0V, I_{S} = 1.7A$		
Static Source-Source On-Resistance	Rss(on)	21	23.4	36	mΩ	V _G S = 3.8V, I _S = 1.7A		
		21.5	23.8	37		V _G S = 3.7V, I _S = 1.7A		
		22	27.6	40		V _{GS} = 3.1V, I _S = 1.7A		
		23	36.8	50		V _G S = 2.5V, I _S = 1.7A		
Diode Forward Voltage	Vss	_	_	1.0	V	V _G S = 0V, I _S = 1.7A		
DYNAMIC CHARACTERISTICS (Note 9)				•		•		
Input Capacitance	Ciss	_	523	_		101/11/		
Output Capacitance	Coss	_	76	_	pF	$V_{SS} = 10V, V_{GS} = 0V,$ f = 1.0kHz		
Reverse Transfer Capacitance	Crss	_	54	_		I = 1.0KHZ		
Total Gate Charge	Qg	_	5.6	_		10)/)/		
Gate-Source Charge	Qgs	_	1.9	_	nC	$V_{DD} = 10V, V_{GS} = 4V,$		
Gate-Drain Charge	Q _{gd}	_	0.4	_		Is = 3.4A		
Turn-On Delay Time	t _{D(ON)}	_	378	_				
Turn-On Rise Time	t _R	_	83	_		$V_{DD} = 10V$, $V_{GS} = 4V$,		
Turn-Off Delay Time	t _{D(OFF)}	_	411	_	ns	Is = 1.7A		
Turn-Off Fall Time	t _F	_	256	_				

Notes:

- 5. Device mounted on FR-4 material with 1inch² (6.45cm²), 2oz. (0.071mm thick) Cu.
- Repetitive rating, pulse width limited by junction temperature.
 Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to production testing.



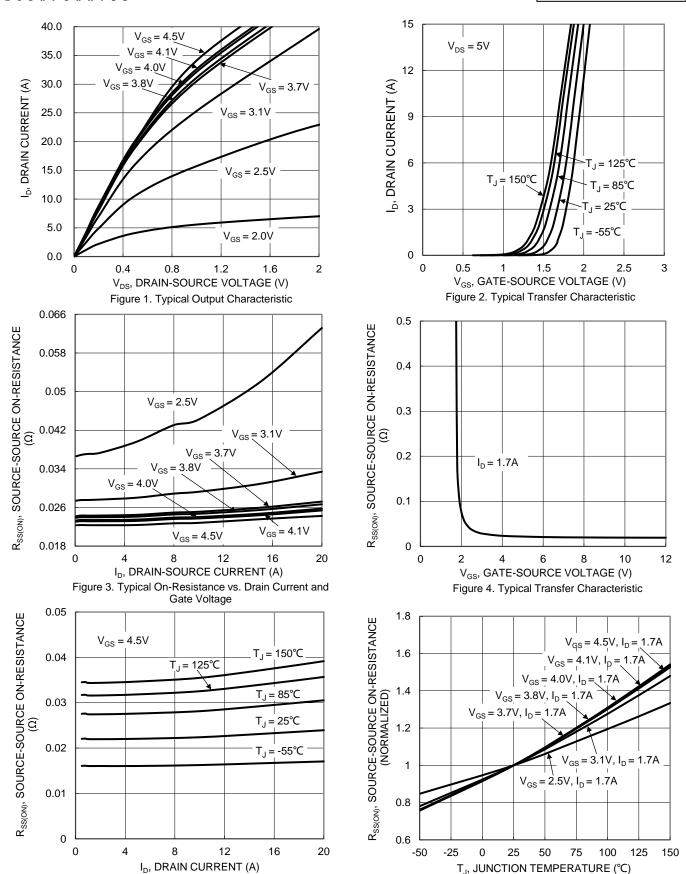
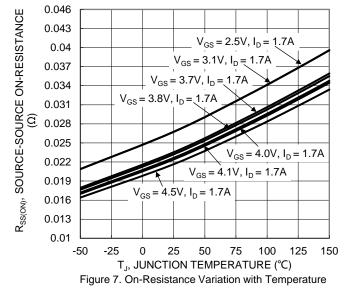


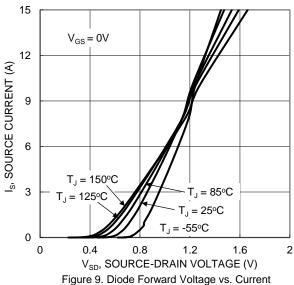
Figure 5. Typical On-Resistance vs. Drain Current and

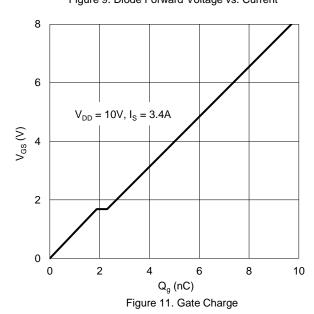
Temperature

Figure 6. On-Resistance Variation with Temperature









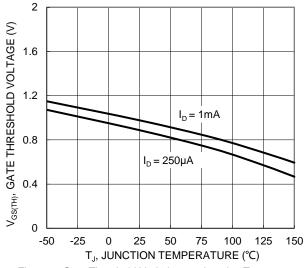
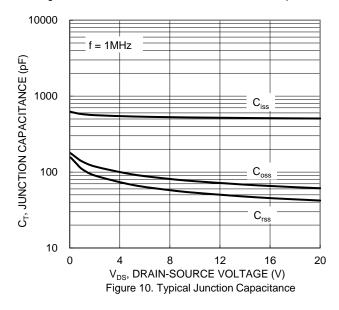
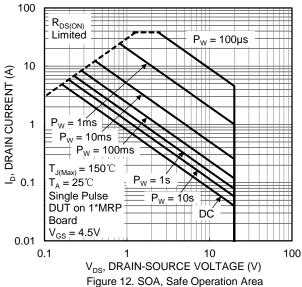


Figure 8. Gate Threshold Variation vs. Junction Temperature







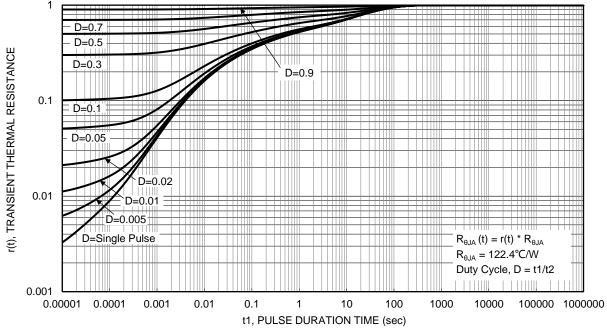


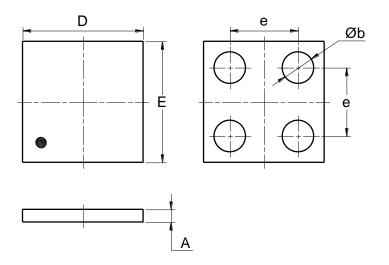
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X4-DSN1111-4

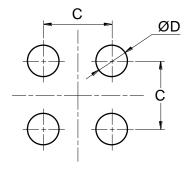


X4-DSN1111-4								
Dim	Min Max Typ							
Α	0.080	0.150	0.120					
b	0.270 0.330 0.300							
D	1.110 1.190 1.150							
Е	1.110 1.190 1.150							
e 0.650 BSC								
All Dimensions in mm								

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X4-DSN1111-4



Dimensions	Value (in mm)
С	0.650
D	0.300



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