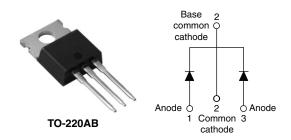


Vishay High Power Products

Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY			
I _{F(AV)} 2 x 20 A			
V _R	45 V		

FEATURES

- 150 °C T_J operation
- · Center tap configuration
- · Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- · Designed and qualified for industrial level

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES			
I _{F(AV)}	Rectangular waveform	40	Α		
V _{RRM}		45	V		
I _{FSM}	$t_p = 5 \mu s sine$	1240	Α		
V _F	20 Apk, T _J = 125 °C (per leg)	0.48	V		
T _J	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	40CTQ045	UNITS	
Maximum DC reverse voltage	V _R		V	
Maximum working peak reverse voltage	V_{RWM}	45 V		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS VALUES UI		UNITS	
Maximum average forward current per leg		50 % duty cycle at T _C = 116 °C, rectangular waveform		20	
See fig. 5 per device	I _{F(AV)}			40	
Maximum peak one cycle non-repetitive		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	1240	A
surge current per leg See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse		350	
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 3 A, L = 4.4 mH		20	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		3	Α

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	20 A	T _J = 25 °C	0.53	V
		40 A		0.68	
		20 A	T _J = 125 °C	0.48	
		40 A		0.67	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	3	mΛ
See fig. 2		T _J = 125 °C		115	mA mA
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.27	V
Forward slope resistance	r _t			8.72	mΩ
Maximum junction capacitance per leg	Ст	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		2800	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to case per leg		Б	D0 "	2.0	
Maximum thermal resistance, junction to case per package		H _{th} JC	R _{thJC} DC operation		°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	
According to the control of				2	g
Approximate weight			0.07	OZ.	
NA 15 1	minimum			6 (5)	kgf ⋅ cm
Mounting torque –	maximum			12 (10)	(lbf \cdot in)
Marking device			Case style TO-220AB	40CTQ045	



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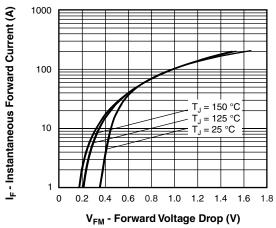


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

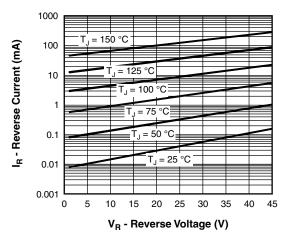


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

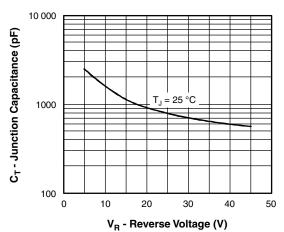


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

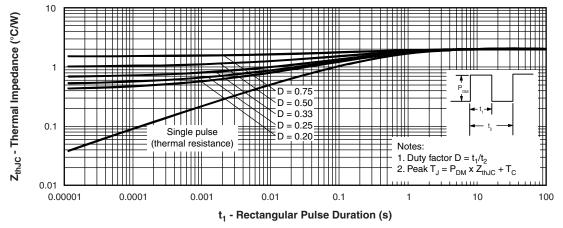


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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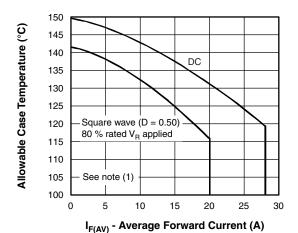


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

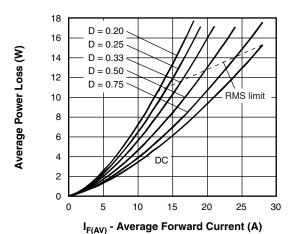


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

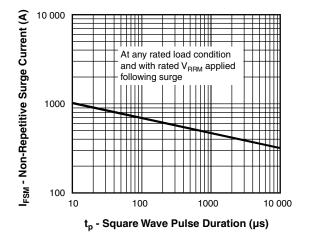


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

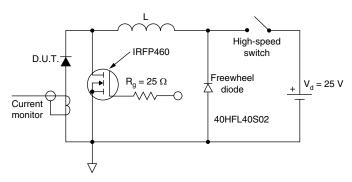


Fig. 8 - Unclamped Inductive Test Circuit

Note

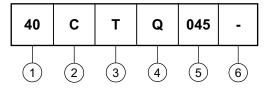
 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 10 V



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ORDERING INFORMATION TABLE

Device code



1 - Current rating (40 = 40 A)

2 - Circuit configuration:

C = Common cathode

Package:

T = TO-220

4 - Schottky "Q" series

5 - Voltage rating (045 = 45 V)

6 - • None = Standard production

• PbF = Lead (Pb)-free

Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS			
Dimensions	http://www.vishay.com/doc?95222		
Part marking information	http://www.vishay.com/doc?95225		



Vishay

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