BLF8G27LS-100P

Power LDMOS transistor

AMPLEON

Rev. 5 — 1 September 2015

Product data sheet

1. Product profile

1.1 General description

100 W LDMOS power transistor for base station applications at frequencies from 2500 MHz to 2700 MHz.

Table 1. Typical performance

Typical RF performance at $T_{case} = 25$ °C in a common source class-AB production test circuit.

| Test signal | f | I _{Dq} | V _{DS} | P _{L(AV)} | Gp | η_{D} | ACPR _{5M} |
|-----------------------|--------------|-----------------|-----------------|--------------------|------|------------|--------------------|
| | (MHz) | (mA) | (V) | (W) | (dB) | (%) | (dBc) |
| Single carrier W-CDMA | 2500 to 2700 | 860 | 28 | 25 | 18 | 33 | -35 ^[1] |

^{[1] 3}GPP test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF. Channel bandwidth is 3.84 MHz.

1.2 Features and benefits

- Excellent ruggedness
- High efficiency
- Low R_{th} providing excellent thermal stability
- Designed for broadband operation (2500 MHz to 2700 MHz)
- Lower output capacitance for improved performance in Doherty applications
- Designed for low memory effects providing excellent pre-distortability
- Internally matched for ease of use
- Integrated ESD protection
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

1.3 Applications

■ RF power amplifiers for W-CDMA base stations and multi carrier applications in the 2500 MHz to 2700 MHz frequency range

2. Pinning information

Table 2. Pinning

| | 3 | | | |
|-----|-------------|---|--------------------|----------------|
| Pin | Description | | Simplified outline | Graphic symbol |
| 1 | drain1 | | 4 0 | |
| 2 | drain2 | | | 1 |
| 3 | gate1 | | 5 | 3 |
| 4 | gate2 | | | 5 |
| 5 | source | Ū | 3 4 | 4 |

^[1] Connected to flange.

3. Ordering information

Table 3. Ordering information

| Type number | Package | e | |
|----------------|---------|--|----------|
| | Name | Description | Version |
| BLF8G27LS-100P | _ | earless flanged ceramic package; 4 leads | SOT1121B |

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | Min | Max | Unit |
|------------------|----------------------|------------|--------------|------|------|
| V_{DS} | drain-source voltage | | - | 65 | V |
| V _{GS} | gate-source voltage | | -0.5 | +13 | V |
| T _{stg} | storage temperature | | -65 | +150 | °C |
| T_j | junction temperature | | <u>[1]</u> _ | 225 | °C |

^[1] Continuous use at maximum temperature will affect the reliability.

5. Thermal characteristics

Table 5. Thermal characteristics

| Symbol | Parameter | Conditions | Тур | Unit |
|----------------------|--|----------------------------------|-------|------|
| R _{th(j-c)} | thermal resistance from junction to case | T_{case} = 80 °C; P_L = 25 W | 0.374 | K/W |

6. Characteristics

Table 6. DC characteristics

 $T_i = 25$ °C; per section unless otherwise specified.

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|----------------------|----------------------------------|--|-----|-----|-----|------|
| V _{(BR)DSS} | drain-source breakdown voltage | $V_{GS} = 0 \text{ V}; I_D = 0.72 \text{ mA}$ | 65 | - | - | V |
| $V_{GS(th)}$ | gate-source threshold voltage | V_{DS} = 10 V; I_{D} = 72 mA | 1.5 | 1.9 | 2.3 | V |
| I _{DSS} | drain leakage current | $V_{GS} = 0 \text{ V}; V_{DS} = 28 \text{ V}$ | - | - | 1.4 | μΑ |
| I _{DSX} | drain cut-off current | $V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 10 \text{ V}$ | - | 14 | - | Α |
| I _{GSS} | gate leakage current | V_{GS} = 11 V; V_{DS} = 0 V | - | - | 140 | nA |
| 9 _{fs} | forward transconductance | V_{DS} = 10 V; I_{D} = 72 mA | - | 0.6 | - | S |
| R _{DS(on)} | drain-source on-state resistance | $V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $I_D = 2.52 \text{ A}$ | - | 0.2 | - | Ω |

Table 7. RF characteristics

Test signal: single carrier W-CDMA; 3GPP test model 1 with 64 DCPH; PAR = 7.2 dB at 0.01 % probability on the CCDF; carrier bandwidth 3.84 MHz; f_1 = 2500 MHz; f_2 = 2600 MHz; f_3 = 2700 MHz; RF performance at $V_{\rm DS}$ = 28 V; $I_{\rm Dq}$ = 860 mA; $T_{\rm case}$ = 25 °C; unless otherwise specified; in a class-AB production test circuit.

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|--------------------|--------------------------------------|--------------------|------|-----|-----|------|
| G_p | power gain | $P_{L(AV)} = 25 W$ | 16.8 | 18 | - | dB |
| RLin | input return loss | $P_{L(AV)} = 25 W$ | - | -12 | -6 | dB |
| η_{D} | drain efficiency | $P_{L(AV)} = 25 W$ | 28 | 33 | - | % |
| ACPR _{5M} | adjacent channel power ratio (5 MHz) | $P_{L(AV)} = 25 W$ | - | -35 | -30 | dBc |

7. Test information

7.1 Ruggedness in class-AB operation

The BLF8G27LS-100P is capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: V_{DS} = 28 V; I_{Dg} = 860 mA; P_{L} = 100 W (CW); f = 2500 MHz.

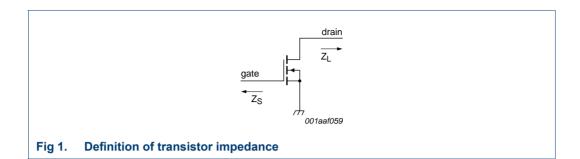
7.2 Impedance information

Table 8. Typical impedance

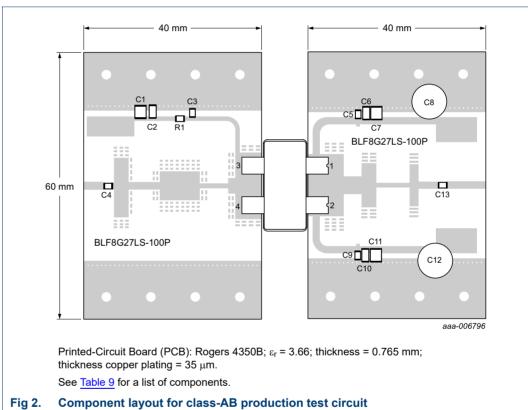
Measured load-pull condition data half device; $I_{Dq} = 430$ mA; $V_{DS} = 28$ V.

| f | Z _S [1] | Z _L [1] |
|-------|--------------------|--------------------|
| (MHz) | (Ω) | (Ω) |
| 2500 | 7.4 – j12.2 | 5 – j10.0 |
| 2600 | 8 – j12.1 | 4.2 – j9.0 |
| 2700 | 11 – j16.9 | 4 – j9.1 |

^[1] Z_S and Z_L defined in Figure 1.



7.3 Test circuit



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Table 9. List of components For test circuit see Figure 2.

| Component | Description | Value | | Remarks |
|---------------------|-----------------------------------|-----------------------|-----|-------------------------|
| C1, C7, C11 | multilayer ceramic chip capacitor | 10 μF | [1] | Murata |
| C2, C6, C10 | multilayer ceramic chip capacitor | 0.1 μF | [1] | Murata |
| C3, C4, C5, C9, C13 | multilayer ceramic chip capacitor | 20 pF | [2] | ATC600F |
| C8, C12 | electrolytic capacitor | 2200 μF , 63 V | | |
| R1, R2 | chip resistor | 9.1 Ω | | Vishay Dale SMD 0805 |

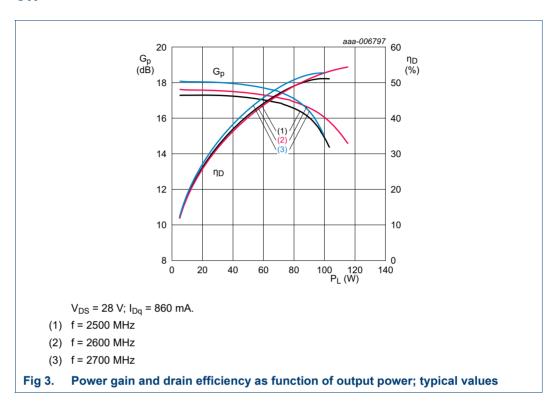
^[1] Murata or capacitor of same quality.

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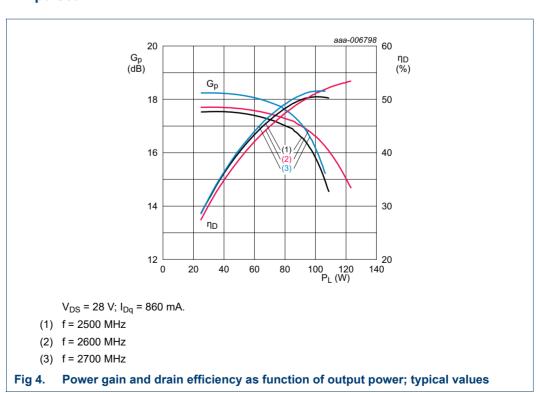
^[2] American Technical Ceramics type 600F or capacitor of same quality.

7.4 Graphical data

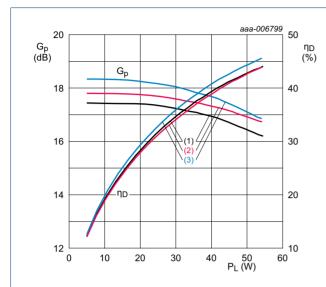
7.4.1 CW



7.4.2 CW pulsed



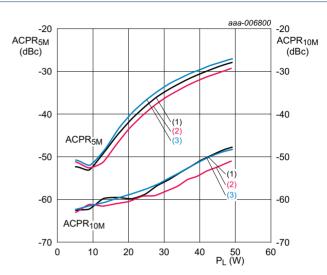
7.4.3 1-Carrier W-CDMA



 V_{DS} = 28 V; I_{Dq} = 860 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 5. Power gain and drain efficiency as function of output power; typical values



 V_{DS} = 28 V; I_{Dq} = 860 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 6. Adjacent channel power ratio (5MHz) and adjacent channel power ratio (10MHz) as function of output power; typical values

8. Package outline

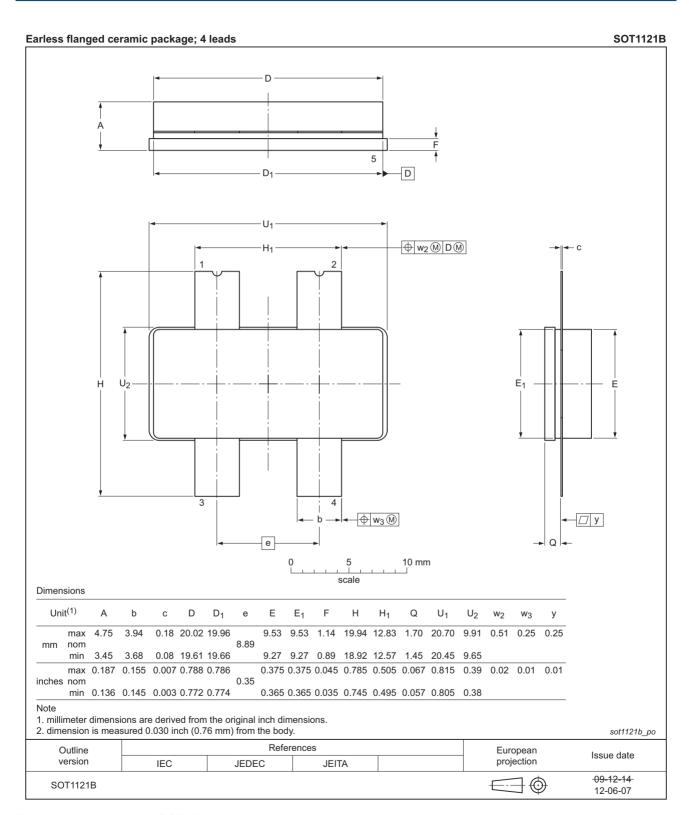


Fig 7. Package outline SOT1121B

9. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

10. Abbreviations

Table 10. Abbreviations

| Acronym | Description |
|---------|--|
| 3GPP | 3rd Generation Partnership Project |
| CCDF | Complementary Cumulative Distribution Function |
| CW | Continuous Wave |
| DPCH | Dedicated Physical CHannel |
| ESD | ElectroStatic Discharge |
| LDMOS | Laterally Diffused Metal Oxide Semiconductor |
| PAR | Peak-to-Average Ratio |
| SMD | Surface Mounted Device |
| VSWR | Voltage Standing Wave Ratio |
| W-CDMA | Wideband Code Division Multiple Access |

11. Revision history

Table 11. Revision history

| Document ID | Release date | Data sheet status | Change notice | Supersedes | |
|--------------------|-----------------------------------|--|---------------|--------------------|--|
| BLF8G27LS-100P#5 | 20150901 | Product data sheet | | BLF8G27LS-100P v.4 | |
| Modifications: | | The format of this document has been redesigned to comply with the new identity guidelines of Ampleon. | | | |
| | Legal texts h | Legal texts have been adapted to the new company name where appropriate. | | | |
| BLF8G27LS-100P v.4 | 20130415 | Product data sheet | - | BLF8G27LS-100P v.3 | |
| BLF8G27LS-100P v.3 | 20130318 | Preliminary data sheet | - | BLF8G27LS-100P v.2 | |
| BLF8G27LS-100P v.2 | 20121220 | Objective data sheet | - | BLF8G27LS-100P v.1 | |
| BLF8G27LS-100P v.1 | 20121203 | Objective data sheet | - | - | |

12. Legal information

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|--------------------------------|-------------------|---|--|
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