



# ENS160

# Digital Metal-Oxide Multi-Gas Sensor



ENS160 datasheet

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The ENS160 is a digital multi-gas sensor solution, based on metal oxide (MOX) technology with four sensor elements.

The independent hotplate control allows the detection of a wide range of volatile organic compounds (VOCs) including ethanol, toluene, hydrogen and oxidizing gases with superior sensitivity. The ENS160 supports intelligent algorithms to process raw sensor measurements on-chip. These algorithms calculate  $CO_2$ -equivalents, TVOC, air quality indices (AQIs) and perform humidity and temperature compensation, as well as baseline management, all on chip.

Raw sensor measurements can be read for further customization. The LGA-packaged device includes SPI and I<sup>2</sup>C slave interfaces to communicate with a main host processor.

The ENS160 is a proven and maintenance-free technology, designed for high volume and reliability.

# Key Features & Benefits

**TrueVOC**<sup>®</sup> air quality detection with industry-leading purity and stability, providing outputs such as  $eCO2^1$ , TVOC and  $AQI^2$  in compliance with worldwide  $IAQ^3$  standards.

**Independent sensor heater control** for highest VOC selectivity and outstanding background discrimination.

#### Immunity to humidity and ozone

- Superior output stability over the whole T and RH operating ranges<sup>4</sup>
- Effective ozone compensation

Hassle-free on-chip heater drive control and data processing - no need for external libraries - no impact on MCU performance.

#### Wide operating ranges

- temperature: -40 to +85°C
- humidity: 5 to 95%<sup>5</sup>
- V<sub>DD</sub>: 1.71 to 1.98V; V<sub>DDIO</sub> 1.71 to 3.6V

<sup>1</sup> eCO2 = equivalent CO2 values for compatibility with HVAC ventilation standards
<sup>2</sup> AQI = Air Quality Index
<sup>3</sup> IAQ = Indoor Air Quality
<sup>4</sup> Further improved by compensation through external T/RH input.

# **Applications**

- Building Automation / smart home / HVAC<sup>6</sup>
  - o Indoor air quality detection
  - Demand-controlled ventilation
  - Smart thermostats
- Home appliances
  - Cooker hoods
  - Air cleaners / purifiers
- IoT devices

## **Properties**

- Small 3x3x0.9mm LGA package
- Standard, fast and fast mode plus  $I^2C$  and SPI interfaces with separate  $V_{\text{DDIO}}$  up to 3.6V
- T&R packaged, reflow solderable<sup>7</sup>.

<sup>6</sup> HVAC = Heat, Ventilation and Air

Conditioning

<sup>7</sup> See section "Soldering Information" for further details.

<sup>&</sup>lt;sup>5</sup> Non-condensing.





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# 1 Block diagram

The ENS160 digital multi-gas sensor consists of four independent heaters and gas sensor elements, based on metal oxide (MOX) technology, and a controller as shown in the functional block diagram below.



#### Figure 1: Functional Blocks

The *Heater Driver* controls the sensor operating modes and provides power to the *heaters* of each individual sensor element. During operation, the heater driver regulates the heaters to their individual set-points.

The Sensor Measurement block determines the value of the sensor resistance for each individual sensor element.

The System Control block processes the resistance values internally to output calculated TVOC, CO<sub>2</sub>-equivalents, AQIs and further signals on the digital interface.

The ENS160 includes a standard 2-wire digital  $I^2C$  interface (SCL, SDA) or 4-wire digital SPI interface (SCLK, MOSI, MISO, CSn) for communication to the main host processor.

On-chip memory is used to store calibration values.





# 2 Pin assignment



## Figure 2: Pin diagram

#### Table 1: Pin description

Pins	Pin Name	Pin Type	Description
1	MOSI / SDA	Input / Output	SPI Master Output Slave Input / I <sup>2</sup> C Bus Bi-Directional Data
2	SCLK / SCL	Input	SPI Serial Clock / I <sup>2</sup> C Bus Serial Clock Input
3	MISO / ADDR	Input / Output	SPI Master Input Slave Output / I <sup>2</sup> C Address Select: I <sup>2</sup> C ADDR pin high -> 0x53 / ADDR pin low -> 0x52
4	V <sub>DD</sub>	Supply	Main Supply Voltage
5	V <sub>DDIO</sub>	Supply	Interface Supply Pins
6	INTn	Output	Interrupt to Host
7	CSn	Input	SPI Interface Select (CSn low -> SPI / CSn high -> I <sup>2</sup> C)
8, 9	V <sub>SS</sub>	Supply	Ground Supply Voltage

Also see sections "I2C operation circuitry" and "SPI operation circuitry" for wiring.



# 3 Absolute maximum ratings

#### Table 2: Absolute Maximum Ratings

Symbol	Parameter	Min Max		Units	Comments		
Electrical Parameters							
V <sub>DD</sub>	Supply Voltage	-0.3	1.98	V			
V <sub>DDIO</sub>	I/O Interface Supply	-0.3	3.6	V			
V <sub>IO1</sub>	MOSI/SDA, SCLK/SCL	-0.3	3.6	V			
V <sub>IO2</sub>	MISO/ADDR, INTn, CSn	-0.3	V <sub>DDIO</sub> +0.3	V			
$V_{SS}$	Input Ground	-0.3	0.3	V			
I <sub>SCR</sub>	Input Current (latch-up immunity)	± 100	± 100		AEC-Q100-004		
Electrostatic Discharge							
ESD <sub>HBM</sub>	Electrostatic Discharge HBM	± 2000		V	JS-001-2014		
ESD <sub>CDM</sub>	Electrostatic Discharge CDM	± 750		V	JS-002-2014		
	Operating a	and Stora	age Condition	IS			
MSL	Moisture Sensitivity Level		1		Unlimited floor lifetime		
T <sub>BODY</sub>	Max. Package Body Temperature		260	°C	IPC/JEDEC J-STD-020		
T <sub>STRG</sub>	Storage Temperature	-40	125	°C			
RH <sub>STRG</sub>	Storage Relative Humidity	5 95		%	Non-condensing		
T <sub>AMB</sub> <sup>8</sup>	Operating Ambient Temperature	-40 85		°C			
RH <sub>AMB</sub> <sup>1</sup>	Operating Ambient Rel. Humidity	5	95	%	Non-condensing		

Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only. Functional operation of the device at these or any other conditions beyond those indicated under Electrical Characteristics is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability and lifetime.

Note: The ENS160 is not designed for use in safety-critical or life-protecting applications.

<sup>&</sup>lt;sup>8</sup> The ENS160 is electrically operable in this range, however its gas sensing performance might vary. Please refer to "Recommended Sensor Operation" for further information.





# 4 Electrical characteristics

The following figure details the electrical characteristics of the ENS160.

#### Table 3: Electrical characteristics

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V <sub>DD</sub>	Positive supply		1.71 <sup>9</sup>	1.8	1.98	V
V <sub>DDIO</sub>	IO Supply Voltage		1.71		3.6	V
I <sub>DD</sub>	Average <sup>10</sup> Supply Current <sup>11</sup>	DEEPSLEEP (OP_MODE 0x00) <sup>12</sup>		0.01		mA
		IDLE (OP_MODE 0x01) <sup>11</sup>		2	2.5	mA
		STANDARD (OP_MODE 0x02)		29		mA
I <sub>DD_PK</sub>	Peak Supply Current <sup>13</sup>	STANDARD (OP_MODE 0x02)		79 (<5ms)		mA
VIH	High-level input voltage		$0.7 x V_{DDIO}$			V
VIL	Low-level input voltage				0.3xV <sub>DDIO</sub>	V
V <sub>OH</sub>	High-level output	MISO <sup>14</sup> [I <sub>OH</sub> =5mA]	0.8xV <sub>DDIO</sub>			V
	voltage	INTN [I <sub>OH</sub> =2mA]	$0.65 x V_{\text{DDIO}}$			V
V <sub>OL</sub>	Low-level output voltage	MOSI/SDA, MISO [I <sub>OL</sub> =5mA]			0.2xV <sub>DDIO</sub>	V
		INTN [I₀∟=2mA]			0.35xV <sub>DDIO</sub>	V

<sup>14</sup> MOSI/SDA is open drain

<sup>&</sup>lt;sup>9</sup> The minimum supply voltage VDD is 1.71V and must not drop below this value. Please refer to the recommended "I2C- or SPI-Operation Circuitry" in section 17.

<sup>&</sup>lt;sup>10</sup> Averaged over the sequence

 $<sup>^{11}</sup>$  Measured at  $V_{\text{DD}}\text{-pin}$  at ambient temperature of 35  $^\circ\text{C}$ 

<sup>&</sup>lt;sup>12</sup> Not a gas sensing mode

<sup>&</sup>lt;sup>13</sup> Initial (<5ms) current demand from VDD after the sensor is switched from IDLE (OP-Mode 1) to STANDARD operation (OP\_MODE 2)



# 5 Air Quality signal characteristics

To satisfy a wide range of individual application requirements, the ENS160 offers a series of (indoor) air quality output signals that are derived from various international, as well as de-facto industry standards. Table 4 provides a summary of such signals, that are further described in the following sections.

#### Table 4: Air Quality signal output characteristics

Parameter	Range	Resolution	Unit	Comment
TVOC	0 - 65,000	1	ppb	For requirements outside these
eCO <sub>2</sub>	400 - 65,000	1	ppm CO <sub>2</sub> .equiv.	specified ranges please contact us
AQI-UBA <sup>15</sup>	1 to 5	1	-	

# 5.1 TVOC - Total Volatile Organic Compounds

More than 5000 VOCs exist, and they are two to five times more likely to be found indoors than outdoors. Indoor VOCs are various types of hydrocarbons from mainly two sources: bio-effluents, i.e. odors from human respiration, transpiration and metabolism, and building material including furniture and household supplies. VOCs are known to cause eye irritation, headache, drowsiness or even dizziness - all summarized under the term Sick Building Syndrome (SBS). Besides industrial applications, comfort aspects (e.g. temperature), or building protection (humidity), VOCs are the main reason for ventilation.

Please refer to white paper "Intelligent Air Quality Beyond  $CO_2$  for Indoor Air Quality" for further information on VOCs.

To group and classify VOCs, regional guidelines and industry preferences define a series of compounds and mixtures as reference, e.g. ethanol, toluene, acetone, combinations of the various groups of VOCs (e.g. ISO16000-29), and others.

Refer to "Registers" and "DATA\_TVOC (Address 0x22)" on how to obtain TVOC values from the ENS160.

## 5.2 eCO2 - Equivalent CO2

Due to the proportionality between VOCs and  $-CO_2$  generated by humans,  $CO_2$ -values historically served as an air quality indicator, reflecting the total amount of VOCs (=TVOC) produced by human respiration and transpiration. This law (first revealed by Max von Pettenkofer<sup>16</sup> in the 19<sup>th</sup> century) and the unavailability of suitable VOC measurement technology made  $CO_2$  the surrogate of inhabitant-generated air-pollution in confined living spaces of the past *and* the present, i.e. today's

<sup>&</sup>lt;sup>15</sup> Classified TVOC output signal according to the indoor air quality levels by the German Federal Environmental Agency (UBA, 2007)

<sup>&</sup>lt;sup>16</sup> Max von Pettenkofer (\*1818 - †1901), German chemist and hygienist.





standard air quality reference for demand-controlled ventilation - as adopted by most HVAC industry standards.



Figure 3: ENS160 equivalent CO2 (eCO2) output vs. NDIR CO2 output during two meeting sessions

The ENS160 reverses the proportional correlation of VOCs and  $CO_2$ , by providing a standardized output signal in ppmCO<sub>2</sub>-equivalents from measured VOCs plus hydrogen, thereby adhering to today's  $CO_2$  standards, as shown in Figure 3.



Figure 4: Added value of ENS160's eCO2 outputs and typical cases of pure CO<sub>2</sub> sensors failing.

A key advantage of the ENS160 is the capture of odors and bio-effluents that are completely invisible to  $CO_2$ -sensors. The diagrams in Figure 4 compare the ENS160's equivalent  $CO_2$  output to an NDIR  $CO_2$  sensor in typical indoor applications.

CO<sub>2</sub> sensors neither detect unpleasant odors and bio-effluents in bedroom or bathroom environments, nor cooking smells in kitchens or restaurants, whereas the ENS160 reliably reports such events.



Proven TrueVOC<sup>®</sup> control algorithms minimize sensor drift and ageing to provide reliable readings over lifetime, thereby making the ENS160's equivalent  $CO_2$  output an affordable solution to complement or substitute real  $CO_2$ -based air quality sensors in the HVAC domain.

Table 5 shows a typical classification of (equivalent) CO<sub>2</sub> output levels.

Table 5: Interpretation of CO<sub>2</sub> and Equivalent CO<sub>2</sub> values

Output		Comment / Recommendation	
eCO <sub>2</sub> / CO <sub>2</sub>	Rating	Comment / Recommendation	
>1500	Bad	Heavily contaminated indoor air / Ventilation required	
1000 - 1500	Poor	Contaminated indoor air / Ventilation recommended	
800 - 1000	Fair	Optional ventilation	
600 - 800	Good	Average	
400 - 600	Excellent	Target	

**Example:** A  $CO_2$ - or  $eCO_2$ -controlled ventilation application would invoke its ventilation fan speeds 1, 2 and 3 at the upper three levels "Fair", "Poor" and "Bad", respectively.

See section "Registers" and "DATA\_ECO2 (Address 0x24)" on how to obtain equivalent CO<sub>2</sub>-values from the ENS160.



# 5.3 AQI-UBA - UBA Air Quality Index

The AQI-UBA<sup>17</sup> air quality index is derived from a guideline by the German Federal Environmental Agency based on a TVOC sum signal. Although a local, German guideline, it is referenced and adopted by many countries and organizations.

Table 6: Air Quality Index of the UBA (German Federal Environmental Agency)<sup>18</sup>

	AQI-UBA	Hygienic Rating	Recommendation	Exposure Limit	
#	Rating	nygleme kating	Recommendation		
5	Unhealthy	Situation not acceptable	Use only if unavoidable Intensified ventilation recommended	Hours	
4	Poor	Major objections	Intensified ventilation recommended Search for sources	<1 month	
3	Moderate	Some objections	Increased ventilation recommended Search for sources	<12 months	
2	Good	No relevant objections	Sufficient ventilation recommended	No limit	
1	Excellent	No objections	Target	No limit	

See section "Registers" and "DATA\_AQI (Address 0x21)" on how to obtain AQI values from the ENS160.

<sup>17</sup> UBA = Umweltbundesamt - German Federal Environmental Agency

<sup>18</sup> Recommendation according to the UBA, Bundesgesundheitsblatt - Gesundheitsforschung Gesundheitsschutz 2007, 50:990-1005, DOI 10.1007/s00103-007-0290-y © Springer Medizin Verlag 2007

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Figure 5: Harmonized, typical response of the ENS160 to various gases

Since metal oxide sensors exhibit a broadband sensitivity to both, reducing and oxidizing gases, their raw output signals represent the resulting sum of the entire gas mixture, present. Such sum-signals are beneficial when it comes to wideband TVOC- or AQI-applications, but unsatisfactory for the detection of single gases.

Figure 5 shows the response of the ENS160 to a variety of individual gases that can be found indoors.

Table 7 provides a list of selected gases that have been individually characterized.

Target Gas	Specified Range	Measurement Range	Unit	Register	Comment
Ethanol	0 to 20	0 to 450	ppm	DATA_ETOH (0x22) = DATA_TVOC	Dedicated Register
Hydrogen	0 to 100	0 to 1,000	ppm	R4 <sub>raw</sub> = GPR_READ[6:7]	R <sub>iraw</sub> = raw resistance values that need to be
Acetone	0 to 20	0 to 450	ppm	R4 <sub>raw</sub> = GPR_READ[6:7]	calibrated to target gas. See text below.
Carbon Monoxide	0 to 900	0 to 900	ppm	R4 <sub>raw</sub> = GPR_READ[6:7]	gas. See lext below.
Toluene	0 to 450	0 to 450	ppm	R4 <sub>raw</sub> = GPR_READ[6:7]	

#### Table 7: Single Gas Signal Characteristics





Measurement values for individual gases can be obtained from dedicated device registers or calculated from sensor raw resistance values as specified in above table. See sections "Registers" and "Gas sensor raw resistance signals" for further information.

# 7 Gas sensor raw resistance signals

For two of its sensing elements the ENS160 provides individual outputs of raw sensor values.

Table 8: Gas sensor raw resistance signals

Sensor	Raw Value	Range	Unit	Gen. Purpose Register	Comment
1	R1 <sub>raw</sub>	[065535]	-	GPR_READ[0:1]	Arbitrary logarithmic units - no resistance values.
4	R4 <sub>raw</sub>	[065535]	-	GPR_READ[6:7]	$R_{\text{iraw}}$ require conversion to corresponding resistance value $R_{\text{ires}}$ [ $\Omega$ ] (see below)

Gas sensor raw values  $R_{iraw}$  can be obtained from the ENS160 General Purpose Read Register (GPR\_READ) for customer-specific signal post-processing.

Prior to use, R<sub>iraw</sub> values require conversion to resistance values, using the following formula:

$$R_{ires}[\Omega] = 2^{\frac{R_{iraw}}{2048}}$$

See section "Registers" and "GPR\_READ (Address 0x48)" on how to obtain AQI values from the ENS160.

# 8 Signal conditioning

Chemical gas sensors are relative sensors that are susceptible to changes in their chemical and physical environments. Typical drivers are changes of the target gas(es), of the interfering background gas mixture and changes of the physical environment (air pressure, humidity, etc.).

In the following sections TrueVOC<sup>®</sup> signal conditioning technology comes into play for enhanced output signal ruggedness and resilience.

## 8.1 Baselining

As part of the TrueVOC<sup>®</sup> technology the ENS160 deploys a unique automatic baseline correction, featuring compensation for oxidizing gases such as ozone.

It furthermore stores the current baseline value in non-volatile memory to automatically start from the latest valid level of background air after re-powering the device and even after a power outage.



#### 8.2 Humidity compensation

Extreme humidity conditions outside this range (20% - 80%RH) can influence the output signal, especially when very accurate or single gas measurements are required. To overcome such impacts, the ENS160 is equipped with a temperature and humidity compensation algorithm, relying on data from an external temperature- and humidity-sensor (the ENS160 works well with the ScioSense ENS21x family of temperature and humidity sensors as they both share the same data format), that can be regularly updated to an internal register for processing.

**Note:** The humidity compensation discussed in this section works per default for all output signals except for sensor raw signals.

See sections "Registers", "TEMP\_IN (Address 0x13)" and "RH\_IN (Address 0x15)" for further information.

#### 8.3 Ozone compensation

Backed up by its multi-sensor architecture, the ENS160 TrueVOC<sup>®</sup> technology deploys an effective ozone compensation algorithm to maintain solid  $eCO_2$ -, TVOC- and AQI-output signals, even during extreme or enduring summer ozone events.

For further background see application note "Effective Ozone Compensation of ENS160's eCO2, TVOC and AQI Outputs".

# 9 Output signal accuracy

The ENS160 exhibits an excellent measurement accuracy<sup>19</sup> and device-to-device variation.

Figure 6 shows the non-linearity of several devices (left) and typical and maximum accuracies (bottom) for various hydrogen concentrations<sup>20</sup>. A typical error of <12% of the measured value can be stated.

<sup>&</sup>lt;sup>19</sup> All values have been determined by tests in clean, partially synthetic air in a climate chamber-with stated environmental conditions, suitable reference analytics and sensor preconditioning of at least 24h, which may not reflect real-life environments. Unless otherwise noted, the accuracy statements have been carried out at 25°C and 50% relative humidity, applying a MOX-sensor-specific calibration scheme.

<sup>&</sup>lt;sup>20</sup> In this document use of the term "Concentration" in ppm (= parts per million) and ppb (= parts per billion) means volume fractions of the respective gases in air: 1 ppm = 1 mL/m<sup>3</sup> = 1000 ppb = 1000  $\mu$ L/m<sup>3</sup>.







Figure 6: Example output signal accuracy for hydrogen

# 10 Start-Up and Response times

#### Table 9: Initial Start-up and Warm-up times

Parameter	Maximum Time	Comment
Initial Start-up	1 hour	Cashalaw far further dataile
Warm-up	3 minutes	See below for further details
Immediate response (T <sub>63</sub> ) <sup>21</sup>	1 second	-

## 10.1 Initial Start-Up

Initial Start-Up is the time the ENS160 needs to exhibit reasonable air quality readings after its first ever power-on.

The ENS160 sensor raw resistance signals and sensitivities will change upon first power-on. The change in resistance is greatest in the first 48 hours of operation. Therefore, the ENS160 employs a start-up algorithm, allowing eCO<sub>2</sub>, TVOC and AQI output signals to be used from first power-on after 1 hour of operation<sup>22</sup>.

 <sup>&</sup>lt;sup>21</sup> Long-term drift of response time: approx. 1s/a; depending on environmental conditions and sensor history.
 <sup>22</sup> Slightly reduced signal accuracy may be encountered in early phase, thereafter.



#### 10.2 Warm-Up

Further to "Initial Start-up" the conditioning or "Warm-up" period is the time required to achieve adequate sensor stability before measuring VOCs after idle periods or power-off. Typically, the ENS160 requires 3 minutes of warm-up until reasonable air quality readings can be expected<sup>23</sup>.

# 11 Gas sensor status and signal rating

The status flag is an additional feature assessing the current operational mode and the reliability of the output signals. It aids the application obligation to manage timings efficiently, in particular during initial start-up or after re-powering. Furthermore, a simple signal quality assessment and a system self-check is provided.

#### Table 10: ENS160 Status and Signal Rating (Validity Flag)

Flag	Meaning	Implementation approach
0	Operating ok	Standard operating mode.
1	Warm-up	During first 3 minutes after power-on.
2	Initial Start-up	During first full hour of operation after initial power-on <sup>24</sup> . Only once in the sensor's lifetime.
3	No valid output	Signals give unexpected values (very high or very low). Multiple sensors out of range.

See "Validity Flag" in section "DEVICE\_STATUS (Address 0x20)" for further information.

# 12 Recommended sensor operation

For best performance, the sensor shall be operated in normal indoor air in the range -5 to  $60^{\circ}$ C (typical: 25°C); relative humidity: 20 to 80%RH (typical: 50%RH), non-condensing with no aggressive or poisonous gases present. Prolonged exposure to environments outside these conditions can affect performance and lifetime of the sensor.

Please also refer to the "ENS160 Design Guidelines and Handling Instructions" document for further information on handling and optimal integration of the ENS160. The guidelines outlined in this document shall be followed for optimal sensor performance and maximum lifetime.

<sup>&</sup>lt;sup>23</sup> Values have been determined by tests in clean, partially synthetic air in a climate chamber-with stated environmental conditions, suitable reference analytics and sensor preconditioning of at least 24h, which may not reflect real-life environments. Unless otherwise noted, the accuracy statements have been carried out at 25°C and 50% relative humidity.

<sup>&</sup>lt;sup>24</sup> Note that the status will only be stored in the non-volatile memory *after* an initial 24h of continuous operation. If unpowered before conclusion of said period, the ENS160 will resume "Initial Start-up" mode after re-powering.





**Important Note:** The ENS160 is not designed for use in any safety-critical or life-protecting application.

# 13 Recommended sensor storage conditions

The guidelines under "Recommended sensor operation" also apply to sensor storage.

## 14 Host communication

The ENS160 is an  $I^2C$  or SPI Slave device.

If the CSn is held high, the interface behaves as an  $I^2C$  slave. At power-up the condition of the MISO/ADDR pin is used to determine the LSB of the  $I^2C$  address. The  $I^2C$  slave address is 0x52 (MISO/ADDR low) or 0x53 (MISO/ADDR high).

If the CSn pin is asserted (low) the interface behaves as an SPI slave. This condition is maintained until the next Power-on Reset.

Both the SPI and I<sup>2</sup>C slave interfaces use the same register map for communication.

14.1 I2C specification

#### 14.1.1 I2C description

The ENS160 is an  $I^2C$  slave device with a fixed 7-bit address 0x52 if the MISO/ADDR line is held low at power-up or 0x53 if the MISO/ADDR line is held high.

The I<sup>2</sup>C interface supports standard (100kbit/s), fast (400kbit/s), and fast plus (1Mbit/s) mode. Details on I<sup>2</sup>C protocol is according to I<sup>2</sup>C-bus specifications [UM10204, I<sup>2</sup>C-bus specification and user manual, Rev. 6, 4 April 2014].

The device applies all mandatory I<sup>2</sup>C protocol features for slaves: START, STOP, Acknowledge and 7bit slave address. None of the other optional features (10-bit slave address, general call, software reset or Device ID) are supported, nor are the master features (Synchronization, Arbitration, START byte).

The Host System, as an I<sup>2</sup>C master, can directly read or write values to one of the registers by first sending the single byte register address. The ENS160 implements "auto increment" which means that it is possible to read or write multiple bytes (e.g. read multiple DATA\_X bytes) in a single transaction.





#### 14.1.2 I2C I/O and timing information

#### Table 11: ENS160 I<sup>2</sup>C I/O parameters

Parameter	Symbol	Standard		Fast		Fast Mode P	lus	Unit
		Min	Max	Min	Max	Min	Max	
Low level input voltage	VIL	-0.5	$0.3 x V_{\text{DDIO}}$	-0.5	$0.3 x V_{\text{DDIO}}$	-0.5	$0.3 x V_{\text{DDIO}}$	V
High level input voltage	VIH	0.7xV <sub>DDIO</sub>	2.39	0.7xV <sub>DDIO</sub>	2.39	0.7xV <sub>DDIO</sub>	2.39	V
Hysteresis of Schmitt trigger inputs	$V_{hys}$	-	-	$0.05 x V_{DDIO}$	-	$0.05 x V_{\text{DDIO}}$	-	V
Low-level output voltage @ 2mA sink current	$V_{OL2}$	-	-	0	0.2xV <sub>DDIO</sub>	0	0.2xV <sub>DDIO</sub>	V
Low-level output current @ 0.4V	I <sub>OL</sub>	3		3		20		mA
Output fall time from $V_{IHmin}$ to $V_{ILmax}$	t <sub>OF</sub>		250	20xV <sub>DDIO</sub> / 5.5	250	20xV <sub>DDIO</sub> / 5.5	250	ns
Input current each I/O pin	li	-10	10	-10	10	-10	10	μA

## Table 12: ENS160 I<sup>2</sup>C timing parameters<sup>25</sup>

Parameter	Symbol	Standard	Standard		Fast		Fast Mode Plus	
		Min	Max	Min	Max	Min	Max	
SCLK clock frequency	f <sub>SCLK</sub>	0	100	0	400	0	1000	kHz
Hold time (repeated) START condition. After this period, the first clock pulse is generated	t <sub>HD_STA</sub>	4	-	0.6	-	0.26	-	μs
LOW period of the SCLK clock	t∟ow	4.7	-	1.3	-	0.5	-	μs
HIGH period of the SCLK clock	t <sub>HIGH</sub>	4.0	-	0.6	-	0.26	-	μs
Set-up time for a repeated START condition	tsu_sta	4.7	-	0.6	-	0.26	-	μs
Data set-up time	$t_{\text{SU}_{\text{DAT}}}$	250	-	100 26	-	50 <sup>2</sup>	-	ns

 $^{25}$  All values referred to  $V_{\text{IHmin}}$  and  $V_{\text{ILmax}}$  levels.

<sup>&</sup>lt;sup>26</sup> A fast mode I<sup>2</sup>C bus device can be used in Standard mode I<sup>2</sup>C bus systems, but the requirement  $t_{SU_DAT} \ge 250$ ns must then be met. This will automatically be the case if the device does not stretch the LOW period of





Data hold-time	thd_dat	0 27	3.45 <sup>28</sup>	0 <sup>3</sup>	0.9 4	0 3	-	μs
Rise time of SDA and SCLK signals	tr	-	1000	20	300	20	120	ns
Fall time of SDA and SCLK signals	t <sub>f</sub>	-	300	20xV <sub>DDIO</sub> / 5.5	300	20xV <sub>DDIO</sub> / 5.5	120	ns
Set-up time for STOP condition	t <sub>su_sto</sub>	4.0	-	0.6	-	0.26	-	μs
Bus free time between a STOP and START condition	t <sub>BUF</sub>	4.7	-	1.3	-	0.5	-	μs
Capacitive load for each bus line	Cb	-	400	-	400	-	550	pF
Noise margin at the LOW level	$V_{nL}$	$0.1 x V_{\text{DDIO}}$	-	$0.1 x V_{\text{DDIO}}$	-	$0.1 \mathrm{xV}_{\mathrm{DDIO}}$	-	V
Noise margin at the HIGH level	$V_{nH}$	$0.2 x V_{\text{DDIO}}$	-	$0.2 x V_{\text{DDIO}}$	-	$0.2 x V_{\text{DDIO}}$	-	V



Figure 7: Definition of I<sup>2</sup>C timing parameters

the SCL signal. If such a device does stretch the LOW period of the SCL signal, it must output the next data bit to the SDA line  $t_{rmax}$ .  $t_{SU_DAT}$ = 1000 + 250 = 1250ns (according to standard mode I<sup>2</sup>C bus specification) before the SCL line is released.

<sup>&</sup>lt;sup>27</sup> This device internally provides a hold time of at least 300ns for the SDA signal to bridge the undefined

region of the falling edge of the SCL. <sup>28</sup> The maximum  $t_{HD_DAT}$  has only to be met if the device does not stretch the LOW period ( $t_{LOW}$ ) of the SCLK signal.



#### 14.1.3 I2C read operation

After the START condition, in the first transaction:

- The I<sup>2</sup>C Master sends the 7-bit slave address and 0 into the R/W bit (the byte sent would be 0xA4 or 0xA6 dependent on the power-up value of MISO/ADDR).
- The I<sup>2</sup>C Master then sends the address of the first register to read.

Then either after a RESTART condition (i.e. STOP followed by START)

- The I<sup>2</sup>C Master sends the 7-bit slave address and 1 into the R/W bit (the byte sent would be 0xA5 or 0xA7 dependent on the power-up value of MISO/ADDR).
- The I<sup>2</sup>C Master then reads 1-n data bytes from sequential registers (if valid) until the transaction is concluded with a STOP condition.



#### Figure 8: I<sup>2</sup>C Read operation



#### Figure 9: I<sup>2</sup>C Auto-increment read operation





#### 14.1.4 I2C write operation

After the START condition, in a single continuous transaction:

- The I<sup>2</sup>C Master sends the 7-bit slave address and 0 into the R/W bit (the byte sent would be 0xA4 or 0xA6 dependent on the power-up value of MISO/ADDR).
- The I<sup>2</sup>C Master then sends the address of the first register to write.
- The I<sup>2</sup>C Master then sends 1-n data bytes which are written into sequential registers (if valid) until the transaction is concluded with a STOP condition.



#### Figure 10: I2C Write operation



#### Figure 11: I<sup>2</sup>C Auto-increment write operation

## 14.2 SPI specification

#### 14.2.1 SPI description

The SPI interface is a slave bus operating up to 10 MHz clock-frequency.

It shares pins with the I<sup>2</sup>C interface. SPI is selected and SPI transfer initiated by asserting the CSn line low. Once the CSn line has been asserted low the ENS160 will not accept I<sup>2</sup>C transactions until the next Power-On Reset.

Data is clocked in on the rising edge of SCLK; most significant bit first.



## 14.2.2 SPI timing information

## Table 13: SPI Timings

Parameter	Symbol	Condition	Min	Тур	Мах	Unit
SPI Clock (SCLK) Frequency	FSCLK				10	MHz
CSn falling to MISO Enabled	TEN	25pF load			20	ns
CSn rising to MISO Disable	TDIS	25pF load			20	ns
MOSI Setup Time before SCLK	TSUPI		15			ns
MOSI hold time after rising SCLK	THLDI		15			ns
CSn low to first rising SCLK	TLEAD		20			ns
Last SCLK low to CSn high	TLAG		20			ns
SCLK High Time	TSCLKH		40			ns
SCLK Low Time	TSCLKL		40			ns
SCLK falling to MISO Valid	TVALID	25pF load			40	ns



Figure 12: SPI Timings Reference





#### 14.2.3 SPI read operation

During a Read operation, data is clocked out on the falling edge of SCLK so it is stable for the following riding edge.

MISO stays in high impedance mode until the device is selected (CSn low). Data on MISO is only valid on a Read operation.

A transaction starts with the target address and R/W control bit in the first byte followed by the read or write data.

In a Read operation Auto-increment of the address enables multiple registers to be read in sequence. CSn de-asserting (to high) terminates the Read sequence.

A Read SPI frame is composed as follows:

#### Table 14: Read SPI frame

Byte	Bit	Name	Description
0	7:1	AD[6:0]	On MOSI: Address of the register to Read
0	0	RW	On MOSI: 1: bytes are to be read, starting from AD[6:0].
1	7:0	RDATA[7:0]	Output on MISO; MOSI ignored
n	7:0	RDATA[7:0]	Output on MISO; MOSI ignored

#### 14.2.4 SPI write operation

In a Write operation, the address does not Auto-increment. Multiple writes can be performed by alternating Address and Data bytes. CSn de-asserting (to high) terminates the Write sequence.

A Write SPI frame is composed as follows:

#### Table 15: Write SPI frame

Byte	Bit	Name	Description
0	7:1	AD[6:0]	On MOSI: Address of the register to Write
0	0	RW	On MOSI: 0: bytes are to be written at AD[6:0].
1	7:0	WDATA[7:0]	Input on MOSI; MISO Dummy Data
even	7:1	AD[6:0]	On MOSI: Address of the register to Write
even	0	RW	On MOSI: 0: bytes are to be Written, at AD[6:0].
odd	7:0	WDATA[7:0]	Input on MOSI; MISO Dummy Data



# 15 Operation

The ENS160 state diagram is depicted in Figure 13. At power-up, the ENS160 configures itself from a reset state and prepares for commands over the serial bus via either I<sup>2</sup>C or SPI Protocols.

The default state is OPMODE 0x01, which is an IDLE condition that enables ENS160 so that it may respond to several commands. In this mode it is not operating as a gas sensor.

OPMODE 0x00 is a very low power standby state, called DEEP SLEEP.

Active OPMODEs are described further in the OPMODE Register section.



#### Figure 13: ENS160 state diagram

**Note:** When the active gas sensing OPMODE (e.g. 0x02 = STANDARD) is running, new data is notified either via the interrupt (INTn) or by polling the DEVICE\_STATUS register. The output of the gas sensing OPMODEs are presented in the DATA\_XXX registers which can be read at any time.

# 16 Registers

This section describes the registers of the ENS160 which enable the host system to

- Identify the Device and version information
- Configure the ENS160 and set the operating mode
- Read back STATUS information, the calculated gas concentrations and Air Quality Index

#### 16.1 Register overview

Note that some registers are spread over multiple addresses. For example, PART\_ID at address 0 is spread over 2 addresses (its "Size" is 2). Registers are stored in little endian so the LSB of PART\_ID is at address 0 and the MSB of PART\_ID is at address 1.





# Table 16: Register Overview

Address	Name	Size	Access	Description
0x00	PART_ID	2	Read	Device Identity 0x01, 0x60
0x10	OPMODE	1	Read / Write	Operating Mode
0x11	CONFIG	1	Read / Write	Interrupt Pin Configuration
0x12	COMMAND	1	Read / Write	Additional System Commands
0x13	TEMP_IN	2	Read / Write	Host Ambient Temperature Information
0x15	RH_IN	2	Read / Write	Host Relative Humidity Information
0x17 – 0x1F	-	1		Reserved
0x20	DEVICE_STATUS	1	Read	Operating Mode
0x21	DATA_AQI	1	Read	Air Quality Index
0x22	DATA_TVOC	2	Read	TVOC Concentration (ppb)
0x24	DATA_ECO2	2	Read	Equivalent CO <sub>2</sub> Concentration (ppm)
0x26	-	2	-	Reserved
0x28	-	2	-	Reserved
0x2A	-	2	Read	Reserved
0x2C – 0x2F	-	1	-	Reserved
0x30	DATA_T	2	Read	Temperature used in calculations
0x32	DATA_RH	2	Read	Relative Humidity used in calculations
0x34 – 0x37	-	1	-	Reserved
0x38	DATA_MISR	1	Read	Data Integrity Field (optional)
0x40	GPR_WRITE[0:7]	8	Read/Write	General Purpose Write Registers
0x48	GPR_READ[0:7]	8	Read	General Purpose Read Registers



- 16.2 Detailed register description
- 16.2.1 PART\_ID (Address 0x00)

This 2-byte register contains the part number in little endian of the ENS160.

The value is available when the ENS160 is initialized after power-up.

#### Table 17: Register PART\_ID

Addres	Address 0x00			
Bits	Field Name	Default	Access	Field Description
0:7	PART_ID_LSB	0x60	read	Lower Byte of Part ID
8:15	PART_ID_MSB	0x01	read	Upper Byte of Part ID

#### 16.2.2 OPMODE (Address 0x10)

This 1-byte register sets the Operating Mode of the ENS160. The Host System can write a new OPMODE at any time.

Any current operating mode will terminate, and the new operating mode will start.

#### Table 18: Register OPMODE

Addres	s 0x10	OPMODE		
Bits	Field Name	Default	Access	Field Description
7:0		0x00	R/W	Operating mode: 0x00: DEEP SLEEP mode (low-power standby) 0x01: IDLE mode (low power) 0x02: STANDARD Gas Sensing Mode 0xF0: RESET

In DEEP SLEEP mode, ENS160 has limited functionality but will respond to an OPMODE write.

Idle Mode is intended for configuration before running an active sensing mode.

0x02 (STANDARD) is an active gas sensing operating mode to indicate the levels of air quality or for specific gas detection.

#### 16.2.3 CONFIG (Address 0x11)

This 1-byte register configures the action of the INTn pin which allows the ENS160 to signal to the host system that data is available.





The INTn pin can be (de-)asserted (polarity configurable) when ENS160 updates GPR\_Read registers, or when it updates DATA registers, or when a certain threshold is reached (set through COMMAND mode).

A typical setting 0x23 would enable an active low interrupt (no pull-up required) when new output data is available in the DATA registers.

#### Table 19: Register CONFIG

Addro	ess 0x11	CONFIG		
Bits	Field Name	Default	Access	Field Description
7	-	0b0	-	Reserved
6	INTPOL	0b0	R/W	INTn pin polarity: 0: Active low (Default) 1: Active high
5	INT_CFG	0b0	R/W	INTn pin drive: 0: Open drain 1: Push / Pull
4	-	0b0		Reserved
3	INTGPR	0b0	R/W	INTn pin asserted when new data is presented in the General Purpose Read Registers
2	-	0b0	-	Reserved
1	INTDAT	0b0	R/W	INTn pin asserted when new data is presented in the DATA_XXX Registers
0	INTEN	0b0	R/W	INTn pin is enabled for the functions above

#### 16.2.4 COMMAND (Address 0x12)

This 1-byte register allows some additional commands to be executed on the ENS160. This register can be written at any time, but commands will only be actioned in IDLE mode (OPMODE 0x01).

The COMMAND register allows multiple interactions with the system where data needs to be passed between the user/host and the ENS160.

Typically, a request for data (e.g. GetHWVer, GetFWVer) will result in the requested data being placed in the General Purpose READ Registers and an input of data (e.g. set alarm threshold) would first be stored in the General Purpose WRITE Registers at address 0x40-47.

Below is a list of valid commands for the ENS160.



#### Table 20: Register COMMAND

Addre	Address 0x12 COMMAND		D	
Bits	Field Name	Default	Access	Command
7:0	Command	0x00	R/W	0x00: ENS160_COMMAND_NOP
				0x0E: ENS160_COMMAND_GET_APPVER – Get FW Version
				0xCC: ENS160_COMMAND_CLRGPR Clears GPR Read Registers

#### 16.2.4.1 ENS160\_COMMAND\_GET\_APPVER

After issuing ENS160\_COMMAND\_GET\_APPVER, the firmware version of the ENS160 will be placed in General Purpose Registers according to table 21. The NEWGPR bit in DEVICE\_STATUS will be set and the INTn asserted if configured to react to NEWGPR.

Table 21: GPR\_READ settings for ENS160\_COMMAND\_GET\_APPVER command

Register	7	6	5	4	3	2	1	0		
GPR_READ4		Version (Major)								
GPR_READ5		Version (Minor)								
GPR_READ6				Version (	Release)					

#### 16.2.4.2 ENS160\_COMMAND\_CLRGPR

After issuing ENS160\_COMMAND\_CLRGPR all GPR Read registers are cleared.

#### 16.2.5 TEMP\_IN (Address 0x13)

This 2-byte register allows the host system to write ambient temperature data to ENS160 for compensation. The register can be written at any time. TEMP\_IN\_LSB should be written first as the update is recognized on a write to TEMP\_IN\_MSB.

#### Table 22: Register TEMP\_IN

	Address 0x13 Bits Field Name		TEMP_IN	TEMP_IN					
			Default	Access	Field Description				
	0:7	TEMP_IN _LSB	0x00	R/W	Lower Byte of TEMP_IN				
	8:15	TEMP_IN_MSB	0x00	R/W	Upper Byte of TEMP_IN				





The format of the temperature data is the same as the format used in the ENS21x (family of ScioSense temperature and humidity sensors) as shown below.

#### Table 23: Format of Temperature Data

Byte 0x14								Byte	Byte 0x13						
7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0
TEMP_IN Integer Part (Kelvin)								TEM	P_IN Fra	ictions					

The ENS160 required input format is: temperature in Kelvin \* 64 (with Kelvin = Celsius + 273.15).

Example: For 25°C the input value is calculated as follows: (25 + 273.15) \* 64 = 0x4A8A.

16.2.6 RH\_IN (Address 0x15)

This 2-byte register allows the host system to write relative humidity data to ENS160 for compensation. The register can be written at any time. RH\_IN\_LSB should be written first as the update is recognized on a write to RH\_IN\_MSB.

Address 0x15 Bits Field Name		RH_IN		
		Default	Access	Field Description
0:7	RH_IN _LSB	0x00	R/W	Lower Byte of RH_IN
8:15	RH_IN_MSB	0x00	R/W	Upper Byte of RH_IN

#### Table 24: Register RH\_IN

The format of the relative humidity data is the same as the format used in the ENS21x as shown below:

#### Table 25: Format of Relative Humidity Data



The ENS160 required input format is: relative humidity in %rH \* 512.

**Example:** For 50% rH the input value is calculated as follows: 50 \* 512 = 0x6400.

#### 16.2.7 DEVICE\_STATUS (Address 0x20)

This 1-byte register indicates the current status of the ENS160.



#### Table 26: Register DEVICE\_STATUS

Addre	ess 0x20	DEVICE_S	STATUS					
Bits	Field Name	Default	Access	Field Description				
7	STATAS	0b0	-	High indicates that an OPMODE is running				
6	STATER	0b0	R	High indicates that an error is detected. E.g. Invalid Operating Mode has been selected.				
5	-	0b0	R	Reserved				
4	-	0b0	R	Reserved				
2-3	VALIDITY FLAG	0600	R	Status 0: Normal operation 1: Warm-Up phase 2: Initial Start-Up phase 3: Invalid output				
1	NEWDAT	0b0	R	High indicates that a new data is available in the DATA_x registers. Cleared automatically at first DATA_x read.				
0	NEWGPR	0b0	R	High indicates that a new data is available in the GPR_READx registers. Cleared automatically at first GPR_READx read.				

During operation, Bit 6 (STATER) of DEVICE\_STATUS is asserted if an error has occurred.

The meaning of the errors may be different, depending on the operation being undertaken.

Further information regarding the error can be read from the GPR\_READ registers.

16.2.8 DATA\_AQI (Address 0x21)

This 1-byte register reports the calculated Air Quality Index according to the UBA.

#### Table 27: Register DATA\_ AQI

Address 0x21		DATA_AQI	DATA_AQI				
Bits	Field Name	Default	Access	Field Description			
0:2	AQI_UBA	0x01	R	Air Quality Index according to UBA [15]			
3:7	Reserved	0x00	R	Reserved			

See section "AQI-UBA - UBA Air Quality Index" for further information.





#### 16.2.9 DATA\_TVOC (Address 0x22)

This 2-byte register reports the calculated TVOC concentration in ppb.

#### Table 28: Register DATA\_TVOC

		DATA_TV	00	
		Default Access		Field Description
0:7	TVOC_LSB	0x00	R	Lower Byte of DATA_TVOC
8:15	TVOC _MSB	C_MSB 0x00 R		Upper Byte of DATA_TVOC

See section "TVOC - Total Volatile Organic Compounds" for further information.

#### 16.2.10 DATA\_ECO2 (Address 0x24)

This 2-byte register reports the calculated equivalent  $CO_2$ -concentration in ppm, based on the detected VOCs and hydrogen.

#### Table 29: Register DATA\_ECO2

Address 0x24 Bits Field Name		DATA_EC	02	
		Default	Access	Field Description
0:7	ECO2_LSB	0x00	R	Lower Byte of DATA_ECO2
8:15 ECO2_MSB		0x00	R	Upper Byte of DATA_ECO2

See section "eCO2 - Equivalent CO2" for further information.

16.2.11 DATA\_ETOH (Address 0x22)

This 2-byte register reports the calculated ethanol concentration in ppb. For dual use the DATA\_ETOH register is a virtual mirror of the ethanol-calibrated DATA\_TVOC register.

#### Table 30: Register DATA\_ETH

Addres	ss 0x22	DATA_ET	ОН		
Bits	Field Name	Default	Access	Field Description	
0:7	ETH_LSB	0x00	R	Lower Byte of DATA_ETH	
8:15	ETH_MSB	0x00	R	Upper Byte of DATA_ETH	



#### 16.2.12 DATA\_T (Address 0x30)

This 2-byte register reports the temperature used in its calculations (taken from TEMP\_IN, if supplied).

#### Table 31: Register DATA\_T

Address 0x30 Bits Field Name		DATA_T	DATA_T				
		Default	Access	Field Description			
0:7	DATA_T _LSB	0x8A	R	Lower Byte of DATA_T			
8:15	8:15 DATA_T_MSB		R	Upper Byte of DATA_T			

The format of the temperature data is the same as the format used in the ENS21x.

#### Table 32: Format of temperature data



The DATA\_T storage format is: temperature in Kelvin \* 64 (with Kelvin = Celsius + 273.15).

**Example:** For a stored DATA\_T value of 0x4A8A the temperature in  $^{\circ}$ C is calculated as follows: 0x4A8A / 64 - 273.15 = 25 $^{\circ}$ C.

See section "TEMP\_IN (Address 0x13)" for further information.

16.2.13 DATA\_RH (Address 0x32)

This 2-byte register reports the relative humidity used in its calculations (taken from RH\_IN if supplied).

#### Table 33: Register DATA\_RH

	Address 0x32 Bits Field Name		DATA_RH		
			Default	Access	Field Description
	0:7	DATA_RH_LSB	0x00	R	Lower Byte of DATA_RH
	8:15	DATA_RH_MSB	0x64	R	Upper Byte of DATA_RH





The format of the relative humidity data is the same as the format used in the ScioSense ENS21x product family.

Table 34: Format of relative humidity data



The DATA\_RH storage format is: relative humidity in %rH \* 512.

**Example:** For a stored DATA\_RH value of 0x6400 the relative humidity in % is calculated as follows: 0x6400 / 512 = 50%rH.

See section "RH\_IN (Address 0x15)" for further information.

16.2.14 DATA\_MISR (Address 0x38)

This 1-byte register reports the calculated checksum of the previous DATA\_\* read transaction (of nbytes). It can be read as a separate transaction, if required, to check the validity of the previous transaction. The value should be compared with the number calculated by the Host system on the incoming Data.

#### Table 35: Register DATA\_MISR

		DATA_MIS	SR	
		Default	Access	Field Description
0:7	DATA_MISR	0x00	R	Calculated checksum of the previous transaction

**Example:** C-code to calculate MISR on the received DATA, to compare with DATA\_MISR:

```
// The polynomial used in the CRC computation in DATA_MISR
// 76543210 bit weight factor
#define POLY 0x1D // 0b00011101 = x^8+x^4+x^3+x^2+x^0 (x^8 is implicit)
// The hardware register DATA_MISR is updated with every read from a
// register in the range 0x20 to 0x37, using a CRC polynomial (POLY).
// For every register read, call `misr_update()` to keep the software
// variable `misr` in sync with the hardware register.
static uint8_t misr = 0; // Mirror of DATA_MISR (0 is hardware default)
uint8_t misr_update(uint8_t data) {
    uint8_t misr_xor= ( (misr<<1) ^ data) & 0xFF;
    if( misr&0x80==0 )
        misr= misr_xor;
    else
```





```
misr= misr_xor ^ POLY;
}
// Typically, when an I2C/SPI transaction is completed, read DATA_MISR,
// and compare it with the software `misr`. They should equal. If not
// there is a CRC error: one or more bytes were corrupted in the transfer.
uint8_t misr_set(void) {
  return misr;
}
// Once the CRC is wrong, or transactions have been executed without
// calling update() the software `misr` is out of sync with DATA_MISR.
// Read DATA_MISR and call `misr_set()` to bring back in sync.
void misr_set(uint8_t * val) {
 misr= val;
}
```

#### 16.2.15 GPR WRITE (Address 0x40)

This 8-byte register is used by several functions for the Host System to pass data to the ENS160. Writes to these registers are not valid when the ENS160 is in DEEP SLEEP or during a low power portion of an operating mode. Writes should only be done during IDLE mode (OPMODE 0x01).

Address 0x	Address 0x40			TE0-7	
Address	Bits	Field Name	Default	Access	Field Description
0x40	0:7	GPR_WRITE0	0x00	R/W	General Purpose WRITE Register 0
0x41	0:7	GPR_WRITE1	0x00	R/W	General Purpose WRITE Register 1
0x42	0:7	GPR_WRITE2	0x00	R/W	General Purpose WRITE Register 2
0x43	0:7	GPR_WRITE3	0x00	R/W	General Purpose WRITE Register 3
0x44	0:7	GPR_WRITE4	0x00	R/W	General Purpose WRITE Register 4
0x45	0:7	GPR_WRITE5	0x00	R/W	General Purpose WRITE Register 5
0x46	0:7	GPR_WRITE6	0x00	R/W	General Purpose WRITE Register 6
0x47	0:7	GPR_WRITE7	0x00	R/W	General Purpose WRITE Register 7

#### Table 36: Register GPR\_WRITE





#### 16.2.16 GPR\_READ (Address 0x48)

This 8-byte register is used by several functions for the ENS160 to pass data to the Host System. When New GPR\_DATA is available the NEW\_GPR bit of the DEVICE\_STATUS register will be set and the INTn pin asserted (if configured).

#### Table 37: Register GPR\_READ

Address 0x48			GPR_READ0-7		
Address	Bits	Field Name	Default	Access Field Description	
0x48	0:7	GPR_READ0	0x00	R	General Purpose READ Register 0
0x49	0:7	GPR_READ1	0x00	R	General Purpose READ Register 1
0x4A	0:7	GPR_READ2	0x00	R	General Purpose READ Register 2
0x4B	0:7	GPR_READ3	0x00	R	General Purpose READ Register 3
0x4C	0:7	GPR_READ4	0x00	R	General Purpose READ Register 4
0x4D	0:7	GPR_READ5	0x00	R	General Purpose READ Register 5
0x4E	0:7	GPR_READ6	0x00	R	General Purpose READ Register 6
0x4F	0:7	GPR_READ7	0x00	R	General Purpose READ Register 7





# 17 Application information

# 17.1 I2C operation circuitry

The recommended application circuit for the ENS160 I<sup>2</sup>C interface operation is shown in Figure 14.



Figure 14: Recommended application circuit (I<sup>2</sup>C operation)

#### Note(s):

- 1. The minimum supply voltage  $V_{DD}$  is 1.71V and must not drop below this value for reliable device operation. Decoupling capacitors must be placed close to the  $V_{DD}$  (Pin 4) and  $V_{DDIO}$  (Pin 5) supply pins of the ENS160.
- 2. CSn must be pulled high (directly to  $V_{DDIO}$ ) to ensure I<sup>2</sup>C interface is selected.
- 3. MISO/ADDR should be pulled low or high to specify the LSB of the address.
- 4. Pull-up resistors.

The above recommendation for pull-up resistance values applies to  $I^2C$  standard mode only. Pull-up resistors for SCL and SDA are assumed to be part of the host system and should be selected dependent on the intended  $I^2C$  data rate and individual bus architecture.





# 17.2 SPI operation circuitry

The recommended application circuit for the ENS160 for SPI interface is shown in Figure 15.



Figure 15: Recommended application circuit (SPI operation)

#### Note(s):

- 1. The minimum supply voltage  $V_{DD}$  is 1.71V and must not drop below this value for reliable device operation. Decoupling capacitors must be placed close to the  $V_{DD}$  (Pin 4) and  $V_{DDIO}$  (Pin 5) supply pins of the ENS160.
- 2. Weak pull-up resistor may be required for MISO to define the level when tri-stated.
- 3. Decoupling capacitors must be placed close to the  $V_{DD}$  (Pin 4) and  $V_{DDIO}$  (Pin 5) supply pins of the ENS160.





# 18 Soldering information

The ENS160 uses an open LGA package. This package can be soldered using a standard reflow process in accordance with IPC/JEDEC J-STD-020D (Figure 16).



#### Figure 16: Solder reflow profile graph

The detailed settings for the reflow profile are shown in Table 38.

#### Table 38: Solder Reflow Profile

Parameter	Reference	Rate / Unit
Average temperature gradient in preheating		2.5K/s
Soak time	t <sub>soak</sub>	23 min
Sock town range	Ts max	200°C
Soak temp range	Ts min	150°C
Time above 217°C (T1)	t <sub>1</sub>	Max. 60s
Time above 230°C (T2)	t <sub>2</sub>	Max. 50s
Time above TPEAK -10°C (T3)	t <sub>3</sub>	Max. 10s
Peak temperature in reflow	T <sub>PEAK</sub>	260°C
Temperature gradient in cooling		Max5K/s

It is recommended to use a no-clean solder paste. There should not be any board wash processes, to prevent cleaning agents or other liquid materials contacting the sensor area.





19 Package drawings & markings



Figure 17: LGA package drawing



## Table 39: LGA package dimensions

Parameter	Symbol	Dimensions			
		Min	Nominal	Мах	
Total thickness	А	-	0.83	0.9	
Dadu Cina	D		3.0	BSC	
Body Size	E		3.0	BSC	
Lead Width	W	0.65	0.7	0.75	
Lead Length	L	0.65	0.7	0.75	
Lead Pitch	е		1.05	BSC	
Lead Count	n		9		
Educ Lond Contra to Contra	D1		2.1	BSC	
Edge Lead Centre to Centre	E1		2.1	BSC	
Note: All dimensions are in mm					



## Figure 18: Recommend LGA Land Pattern for ENS160

Note(s):

1. All dimensions are in millimeters.





- 2. PCB land pattern are shown as red dotted lines.
- 3. Add 0.05mm all around the nominal lead width and length for the PCB land pattern.



Figure 19: LGA package marking

# 20 Ordering information

#### Table 40: Ordering information

Ordering Code	Material ID	Package	Delivery Form	Delivery Quantity
ENS160-BGLM	507870026	9-pin LGA	Tape & reel	500 pcs
ENS160-BGLT	507870029	9-pin LGA	Tape & reel	1,500 pcs
ENS160-BGLR	507870030	9-pin LGA	Tape & reel	5,000 pcs
ENS160-LG_EK_ST V1	507870028	PCB	box	1 рс

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# 21 RoHS Compliance & ScioSense Green Statement

**RoHS:** The term RoHS compliant means that Sciosense B.V. products fully comply with current RoHS directives. Our semiconductor products do not contain any chemicals for all 6 substance categories, including the requirement that lead does not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, RoHS compliant products are suitable for use in specified lead-free processes.

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# 23 Document status

#### Table 41: Document status

Document Status	Product Status	Definition
Product Preview	Pre-Development	Information in this datasheet is based on product ideas in the planning phase of development. All specifications are design goals without any warranty and are subject to change without notice.
Preliminary Datasheet	Pre-Production	Information in this datasheet is based on products in the design, validation or qualification phase of development. The performance and parameters shown in this document are preliminary without any warranty and are subject to change without notice.
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# 24 Revision information

#### Table 42: Revision history

Revision	Date	Comment	Page
1.1	2022-03-30	Rename DATA_STATUS to DEVICE_STATUS. Note on min. supply voltage Correct current consumption	29, 30 7, 36, 37 7
1.0	2021-10-20	Official release	All
0.95	2020-12-09	Preliminary Version – Product Launch	All
0.9	2019-12-11	Initial Preliminary Version	All

#### Note(s) and/or Footnote(s):

- 1. Page and figure numbers for the previous version may differ from page and figure numbers in the current revision.
- 2. Correction of typographical errors is not explicitly mentioned.



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