CTMMP5015F Series From .47µH to 10µH



CHARACTERISTICS

Description: SMD (shielded) power inductor.

Applications: PDA, Notebook, Desktop, Server applications, Low profile, high current power supplies, battery powered devices, DC/DC converter for Field Programmable Gate Array (FPGA). Operating Temperature: -55°C to +125°C (The part temperature (ambient + temp. rise) should not exceed 125°C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application)

Inductance Tolerance: ±20%

Testing: Inductance is tested on an HP4285A at 200KHz, 0.25V Packaging: Tape & Reel.

Marking: Parts are marked with inductance code.

Miscellaneous: RoHS Compliant.

Additional Information: Additional electrical & physical information available upon request.

Samples available. See website for ordering information.

SPECIFICATIONS

Parts are available in ±20% inductance tolerance only. *Irms DC current (A) that will cause an approximate ∆T of 40°C. **Isat DC current (A) that will cause L0 to drop approximately 30%.

Part Number	Inductance (µH)	L Test Freq. (KHz)	DCR Max. (mΩ)	*Irms Typ. (A)	**lsat Typ. (A)
CTMMP5015F-R47M	0.47	200	2.0	32.0	55.0
CTMMP5015F-R82M	0.82	200	3.0	25.0	44.0
CTMMP5015F-1R0M	1.00	200	3.3	24.0	40.0
CTMMP5015F-3R3M	3.30	200	12.0	12.0	22.0
CTMMP5015F-5R6M	5.60	200	18.0	9.5	16.0
CTMMP5015F-100M	10.0	200	34.0	7.0	12.0

	PHYS		NENSION	IS (R47M ~	1 ROM)	
Size	A Max.	B Max.	C Max.	D	E Ref.	F
mm	14.0	12.9	3.8	3.0±0.5	8.4	2.0±0.5
inches	0.55	0.51	0.15	0.12±0.02	0.33	0.08±0.02
	PHYS	ICAL DIN	NENSION	15 (3R3M ~	· 100M)	

Size	A Max.	В Мах.	C Max.	` D	E Ref.	F Ref.
mm	13.8	12.9	3.8	4.7±0.3	8.4	2.4
inches	0.54	0.51	0.15	0.18±0.012	0.33	0.10



PAD LAYOUT



