PCN Number:		20190920002				PCN Date: Sept. 27, 2			019	
Title					3222, ADC	3223, ADC322	_		,	
	omer Contact:		Manac		,	,		De	pt:	Quality Services
	nge Type:									- Quanty Controco
-	Assembly Site				Design			П	Wafer	Bump Site
=	Assembly Process						Ħ	Wafer Bump Material		
	Assembly Materials		Ħ	Part number change			П		Bump Process	
	Mechanical Specification				Test Site			Ħ		Fab Site
	Packing/Shipping/Labeling				Test Process					Fab Materials
							Wafer	Fab Process		
				No	otificatio	n Details				
Desc	ription of Chang	e:								
TEXAS INSTRUMENTS ADC3221, ADC3222, ADC3223, ADC3224 SBAS672D – JULY 2014–REVISED SEPTEMBER 2019 Changes from Povision C (July 2019) to Povision D										
-	Changes from Revision C (July 2019) to Revision D Deleted Graphs: Histogram, Integral Nonlinearity, and Differential Nonlinearity									
			_		earity, and Differential Nonlinearity					
Deleted Graphs: Histogram, Integral Nonlinearity, and Differential Nonlinearity Deleted Graphs: Histogram, Integral Nonlinearity, and Differential Nonlinearity										
Deleted Graphs: Histogram, Integral Nonlinearity, and Differential Nonlinearity										
Ch	anges from Revision B ((March	2016) to	Revi	sion C					Page
•	Added text to the Descrip	otion: O	otionally,	a one	e-wire serial LV	DS interface is avail	able			1
•	Changed the description of pin AVDD, DVDD, GND, and PDN pins in the Pin Functions table									
•	Changed the condition statement for <i>Electrical Characteristics</i> : <i>General</i>									
•	Moved the location of Electrical Characteristics: General									
•	Changed the parameter description of E _{G(REF)} in <i>Electrical Characteristics: General</i>									
•	Deleted E _{G(CHAN)} from Electrical Characteristics: General									
•	Changed the parameter description of α _(EGCHAN) in <i>Electrical Characteristics: General</i>									
:	Changed the condition statement for <i>Electrical Characteristics: ADC3221, ADC3222.</i> Changed ADC clock frequency (ADC3241) From: MAX = 125 MSPS To: MAX = 25 MSPS in <i>Electrical</i>									
Characteristics: ADC3221, ADC3222 Changed ADC clock frequency (ADC3242) From: MAX = 125 MSPS To: MAX = 50 MSPS in Electrical Characteristics: ADC3231, ADC3232										

Changed the condition statement for Electrical Characteristics: ADC3221						
Changed the condition statement for Electrical Characteristics: ADC3222						
Changed the condition statement for Electrical Characteristics: ADC3223						
Changed the condition statement for Electrical Characteristics: ADC3224						
Added Differential swing to DIGITAL INPUTS (SYSREFP, SYSREFM)						
Deleted V _{IH} and V _{IL} from DIGITAL INPUTS (SYSREFP, SYSREFM)						
added table note: SYSREF is internally biased to 0.9 V.to Digital Characteristics						
Added Graphs: Histogram, Integral Nonlinearity, and Differential Nonlinearity						
Added Graphs: Histogram, Integral Nonlinearity, and Differential Nonlinearity						
Added Graphs: Histogram, Integral Nonlinearity, and Differential Nonlinearity						
Added Graphs: Histogram, Integral Nonlinearity, and Differential Nonlinearity						
Changed the Overview section						
Added Using the SYSREF Input section						
Changed the Register Initialization through SPI section						
Changed the Detailed Design Procedure section						
The datasheet number will be changing.						
Device Family	Change From:	Change To:				
ADC3221, ADC3222, ADC3223,	SBAS672B	SBAS672D				
ADC3224						
These changes may be reviewed at the datasheet links provided.						
http://www.ti.com/product/ADC3221						
Reason for Change:						
To accurately reflect device character	ristics.					

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

ADC3221IRGZR	ADC3221IRGZT	ADC3222IRGZR	ADC3222IRGZT				
ADC3223IRGZR	ADC3223IRGZT	ADC3224IRGZR	ADC3224IRGZT				

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