PRODUCT CHANGE NOTICE

Data Sheet Specification Change for Intersil Product ISL21090BFB812Z-TK

Refer to: PCN13021

Date: March 1, 2013



March 1, 2013

To: Our Valued Intersil Customer

Subject: Data Sheet Specification Change for Intersil Product ISL21090BFB812Z-TK

This notice is to inform you that Intersil has changed the data sheet specification for the ISL21090BFB812Z-TK product. The changes remove the *ISC-Short Circuit Current* and $\Delta VOUT/\Delta IOUT$ Load Regulation - Sinking parameters in the Electrical Specifications table for the 1.25V option and are necessary to maintain product manufacturability in support of customer delivery requirements. Details regarding the changes are contained on the following page. The updated data sheet is available on the Intersil web site at http://www.intersil.com/content/dam/Intersil/documents/fn69/fn6993.pdf.

There have been no changes made to the die/silicon. There will be no change in external marking of the packaged parts. Parts affected by this change are identifiable via Intersil's internal traceability system.

Intersil will take all necessary actions to conform to agreed upon customer requirements and to ensure the continued high quality and reliability of Intersil products being supplied. Customers may expect to receive product electrically screened to the revised data sheet beginning *ninety* days from the date of this notification or earlier with approval.

If you have concerns with this notice, Intersil must hear from you promptly. Please contact the nearest Intersil Sales Office or call the Intersil Corporate line at 1-888-468-3774, in the United States, or 1-321-724-7143 outside of the United States.

Regards.

Jon Brewster

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Intersil Corporation PCN13021

CC: J. Touvell K. Mouloua M. Carmody P. Lee



PA13021 Data Sheet Updates

From:

Electrical Specifications $V_{IN} = 5V$ (1.25V option), $I_{OUT} = 0$, $C_L = 0.1 \mu F$ and $C_C = 0.01 \mu F$, unless otherwise specified. **Boldface limits apply** over the operating temperature range, -40 °C to +125 °C.

PARAMETER	DESCRIPTION	CONDITIONS	MIN (Note 7)	TYP	MAX (Note 7)	UNIT
V _{OUT}	Output Voltage	V _{IN} = 5V,		1.25		V
V _{OA}	V _{OUT} Accuracy @ T _A = +25 °C (Note 6)	V _{OUT} = 1.25V	-0.03		+0.03	96
TC V _{OUT}	Output Voltage Temperature Coefficient (Note 8)	ISL21090 B grade			7	ppm/°C
V _{IN}	Input Voltage Range	V _{OUT} = 1.25V	3.7		36	v
I _{IN}	Supply Current			0.750	1.28	mA
$\Delta V_{OUT}/\Delta V_{IN}$	Line Regulation	V _{IN} = 3.7V to 36V, V _{OUT} = 1.25V		6	17	ppm/V
$\Delta V_{OUT}/\Delta I_{OUT}$	Load Regulation	Sourcing: 0mA ≤ I _{OUT} ≤ 20mA		2.5	17	ppm/mA
		Sinking: -10mA ≤ I _{OUT} ≤ 0mA		2.5	17	ppm/mA
V _D	Dropout Voltage (Note 9)	V _{OUT} = 1.25V @ 10mA		1.7	2.15	v
I _{SC+}	Short Circuit Current	T _A = +25 °C, V _{OUT} tied to GND		53		mA
I _{SC-}	Short Circuit Current	T _A = +25 °C, V _{OUT} tied to V _{IN}		-23		mA
t _R	Turn-on Settling Time	90% of final value, C_L = 1.0 μ F, C_C = open		150		μs
	Ripple Rejection	f = 120Hz		90		dB
e _{np-p}	Voltage Noise	0.1Hz ≤ f ≤ 10Hz, V _{OUT} = 1.25V		1.0		μV _{P-P}
V _n	Broadband Voltage Noise	10Hz ≤ f ≤ 1kHz, V _{OUT} = 1.25V		1.2		μV _{RMS}
e _n	Noise Voltage Density	f = 1kHz, V _{OUT} = 1.25V		35.4		nV/√Hz
$\Delta V_{OUT}/\Delta t$	Long Term Stability	T _A = +25 ° C		20		ppm

To:

Electrical Specifications V_{IN} = 5V (1.25V option), I_{OUT} = 0, C_L = 0.1 μ F and C_C = 0.01 μ F, unless otherwise specified. **Boldface limits apply** over the operating temperature range, -40 °C to +125 °C.

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e _n	Noise Voltage Density	f = 1kHz, V _{OUT} = 1.25V		25		nV/√Hz
$\Delta V_{OUT}/\Delta t$	Long Term Stability	T _A = +25 ° C		20		ppm

